

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
5 August 2004 (05.08.2004)

PCT

(10) International Publication Number
WO 2004/065671 A1

(51) International Patent Classification⁷: **D01F 9/12**
(21) International Application Number:
PCT/US2003/000839

(22) International Filing Date: 13 January 2003 (13.01.2003)

(25) Filing Language: English

(26) Publication Language: English

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(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZM, ZW.

(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:
— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

WO 2004/065671 A1

(54) Title: **CARBON NANOTUBE FILMS, LAYERS, FABRICS, RIBBONS, ELEMENTS AND ARTICLES**

(57) Abstract: Carbon Nanotube Films, Layers, Fabrics, Ribbons, Elements and Articles are disclosed. To make various articles, certain embodiments provide a substrate. Preformed nanotubes are applied to a surface of the substrate to create a non-woven fabric of carbon nanotubes. Portions of the non-woven fabric are selectively removed according to a defined pattern to create the article. To make a nanofabric, a substrate is provided. Preformed nanotubes are applied to a surface of the substrate to create a non-woven fabric of carbon nanotubes wherein the non-woven fabric is substantially uniform density. The nanofabrics and articles have characteristics desirable for various electrical systems such as memory circuits and conductive traces and pads.

Carbon Nanotube Films, Layers, Fabrics, Ribbons, Elements and Articles

Background

1. Technical Field

The present invention relates generally to nanotube films, layers, and fabrics and methods of making same and, more specifically to carbon nanotube films, layers, and fabrics and methods of making same so that they form or may be made to form patterned ribbons, elements and articles of various shapes and characteristics.

2. Discussion of Related Art

Wire crossbar memory (MWCm) has been proposed. (See U.S. Patent Nos. 6,128,214; 6,159,620; and 6,198,655.) These memory proposals envision molecules as bi-stable switches. Two wires (either a metal or semiconducting type) have a layer of molecules or molecule compounds sandwiched in between. Chemical assembly and electrochemical oxidation or reduction are used to generate an "on" or "off" state. This form of memory requires highly specialized wire junctions and may not retain non-volatility owing to the inherent instability found in redox processes.

More recently, memory devices have been proposed which use nanoscopic wires, such as single-walled carbon nanotubes, to form crossbar junctions to serve as memory cells. (See WO 01/03208, Nanoscopic Wire-Based Devices, Arrays, and Methods of Their Manufacture; and Thomas Rueckes et al., "Carbon Nanotube-Based Nonvolatile Random Access Memory for Molecular Computing," Science, vol. 289, pp. 94-97, 7 July, 2000.) Hereinafter these devices are called nanotube wire crossbar memories (NTWCMs). Under these proposals, individual single-walled nanotube wires suspended over other wires define memory cells. Electrical signals are written to one or both wires to cause them to physically attract or repel relative to one another. Each physical state (i.e., attracted or repelled wires) corresponds to an electrical state. Repelled wires are an open circuit junction. Attracted wires are a closed state forming a rectified junction. When electrical power is removed from the junction, the wires retain their physical (and thus electrical) state thereby forming a non-volatile memory cell.

The NTWCM proposals rely on directed growth or chemical self-assembly techniques to grow the individual nanotubes needed for the memory cells. These

techniques are now believed to be difficult to employ at commercial scales using modern technology. Moreover, they may contain inherent limitations such as the length of the nanotubes that may be grown reliably using these techniques, and it may be difficult to control the statistical variance of geometries of nanotube wires so grown. Improved memory cell designs are thus desired.

The reliable fabrication of electrically conductive, ultra-thin metallic layers and electrodes in the sub-10 nm regime is problematic. (See, e.g., S. Wolf, *Silicon Processing for the VLSI era; Volume 2 – Process Integration*, Lattice Press, Sunset Beach, 1990.) Metal films in this size regime are usually non-continuous and not conductive over macroscopic distances. Furthermore, these sub-10 nm films are prone to thermal damage by electrical current, making them unsuitable for applications such as electrical interconnects in semiconductor devices. Thermal damage of thin metal interconnects caused by their low heat conductivities is one of the main factors inhibiting dramatic miniaturization and performance improvements of highly integrated semiconductor devices.

Conventional interconnect technologies have a tendency to suffer from thermal damage and metal diffusion eroding the performance of the semiconductor devices especially from degradation of the electrical properties. These effects become even more pronounced with size reduction in current generation 0.18 μm and 0.13 μm structures, e.g. by metal diffusion through ultra-thin gate oxide layers.

There is therefore a need in the art for conductive elements that may operate well in contexts having high current densities or in extreme thermal conditions. This includes circuit contexts with very small feature sizes but includes other high current density, extreme thermal environment contexts as well. There is also a need for conductive elements that will be less likely to diffuse undesirable amounts of contaminants into other circuit elements.

Summary

The invention provides new carbon nanotube films, layers, fabrics, ribbons, elements and articles.

According to one aspect of the invention, a conductive article has a plurality of nanotube segments in contact to define a plurality of conductive pathways along the article and the article has a shape of pre-defined pattern.

According to another aspect of the invention, an assembly includes a substrate and a conductive trace disposed on the substrate. The trace includes nanotube segments that contact other nanotube segments to define a plurality of conductive pathways along the trace. The trace has a shape of pre-defined pattern.

According to another aspect of the invention, a conductive trace includes an electrical network of nanotubes in contact with other nanotubes to define a plurality of conductive pathways along the trace. The trace has a shape of pre-defined pattern.

According to another aspect of the invention, the nanotube segments include single walled carbon nanotubes.

According to other aspects of the invention, the article or trace is primarily a monolayer of nanotube segments.

According to other aspects of the invention, the article or trace is about 2nm or less in thickness.

According to other aspects of the invention, the article or trace has nanotube segments that include multi-walled carbon nanotubes.

According to other aspects of the invention, the article or trace has nanotube segments that have different lengths.

According to other aspects of the invention, the article or trace has nanotube segments that include segments having a length shorter than the length of the article.

According to other aspects of the invention, the article or trace has nanotube segments that have a conformal characteristic to conform to non-planar surfaces.

According to other aspects of the invention, the article or trace has a resistance of about 1-1000 k Ω /□.

According to other aspects of the invention, the article or trace has a substantially uniform density of nanotube segments.

According to other aspects of the invention, a non-woven fabric of carbon nanotubes are in contact and the fabric has substantially uniform density.

According to other aspects of the invention, the non-woven fabric has a controlled composition of metallic nanotubes relative to semiconducting nanotubes.

According to other aspects of the invention, the non-woven fabric is primarily single-walled carbon nanotubes.

According to other aspects of the invention, the non-woven fabric is primarily multi-walled carbon nanotubes.

According to other aspects of the invention, the non-woven fabric is primarily a monolayer of nanotube segments.

According to other aspects of the invention, the non-woven fabric is primarily a multilayer of nanotube segments.

According to other aspects of the invention, the non-woven fabric is about 2nm or less in thickness.

According to other aspects of the invention, the non-woven fabric has a conformal characteristic to conform to non-planar surfaces.

According to other aspects of the invention, the non-woven fabric has a resistance of about 1-1000 k Ω /□.

Brief Description of the Drawings

In the Drawing,

Figure 1A shows a structure, according to certain embodiments of the invention, having a thin layer of metal catalyst that may be used in an exemplary method of growing nanofabric.

Figures 1B.1-1B.2 illustrate exemplary methods of growing nanotube fabric by CVD using the structure of Figure 1A.

Figures 1C-1Z are micrographs of nanofabrics grown with exemplary processes according to certain embodiments of the invention.

Figure 2 is a cross-sectional view of an exemplary structure used to practice certain embodiments of the invention.

Figure 3A shows a structure, according to certain embodiments of the invention, having a distribution of nanoparticles that may be used in an exemplary method of growing nanofabric.

Figure 3B-C illustrate exemplary methods of growing nanotube fabric by CVD using the structure of Figure 3A.

Figures 3D-3F are micrographs of nanofabrics grown with exemplary processes according to certain embodiments of the invention.

Figure 3G-H illustrate exemplary methods of growing nanotube fabric by CVD using the structure of Figure 3A.

Figure 4A shows a structure, according to certain embodiments of the invention, having a thin layer of metal catalyst and having a distribution of nanoparticles that may be used in an exemplary method of growing nanofabric.

Figures 4B-D illustrate exemplary methods of growing nanotube fabric by CVD using the structure of Figure 4A.

Figure 5A shows a structure, according to certain embodiments of the invention, in which a nanofabric is formed over a substrate.

Figure 5B illustrates an exemplary method of forming nanotube fabric by spin-coating preformed nanotubes in suspension.

Figures 5C-5H are micrographs of nanofabrics formed with exemplary processes according to certain embodiments of the invention.

Figures 5I-J illustrate exemplary methods of forming nanotube fabric by spin-coating preformed nanotubes in suspension.

Figure 6 are cross-sectional views of exemplary structures according to certain embodiments of the invention.

Figure 7 shows cross-sectional views of exemplary structures according to certain embodiments of the invention.

Figure 8A shows cross-sectional views of exemplary structures according to certain embodiments of the invention.

Figures 8B-D are micrographs of nanofabrics patterned according to certain embodiments of the invention.

Detailed Description

Preferred embodiments of the invention provide nanotube films, layers, or non-woven fabrics and methods of making same so that they form, or may be made to form, various useful patterned components, elements or articles. (Hereinafter “films,” “layers,” or “non-woven fabrics” are referred to as “fabrics” or “nanofabrics”.) The components created from the nanofabrics retain desirable physical properties of the nanotubes and/or the nanofabrics from which they are formed. In addition, preferred embodiments allow modern manufacturing techniques (e.g., those used in semiconductor manufacture) to be readily employed to utilize the nanofabric articles and devices.

For example, the nanofabrics may be patterned into ribbons, which can be used to create non-volatile electromechanical memory cells. As explained in U.S. Pat. Apl. Ser. Nos. 09/915,093 and 10/033,323 (incorporated by reference in their entireties), the ribbons may be used as a component of a non-volatile electromechanical memory cell. The deflected, physical state of the ribbon may be made to represent a corresponding information state. The deflected, physical state has non-volatile properties, meaning the ribbon retains its physical (and therefore informational) state even if power to the memory cell is removed. The nanofabric may also be formed into conductive traces or pads. As explained in U.S. Pat. Apl. Ser. Nos. 10/128,118 and 10/175,586 (incorporated by reference in their entireties), the trace has advantageous electrical and thermal

conductivity, allowing it to be used for extremely small feature sizes, or to be utilized as a transistor component, such as a gate or base of a transistor forming superior metal to semiconductor contacts. The nanofabrics may also be formed or patterned into shorter segments such as ribbons or patches. The shorter segments or patches allow facile interconnection of their nanotubes to vias, interconnects, traces or other structures useful in electronic devices. They may also be used to create new forms of electromechanical memory cells, for example, non-crossbar, embedded cells. The articles so formed help enable the generation of nanoelectronic devices and may also be used to assist in increasing the efficiency and performance of current electronic devices using a hybrid approach (e.g., using nanoribbon memory cells in conjunction with semiconductor addressing and processing circuitry).

Preferred nanofabrics have a plurality of nanotubes in contact so as to form a non-woven fabric. Gaps in the fabric, i.e., between nanotubes either laterally or vertically, may exist. The fabric preferably has a sufficient amount of nanotubes in contact so that at least one electrically conductive, semi-conductive or mixed conductive and semi-conductive pathway exists from a given point within a ribbon or article to another point within the ribbon or article (even after patterning of the nanofabric).

Though certain embodiments prefer single-walled nanotubes in the nanofabrics, multi-walled nanotubes may also be used. In addition, certain embodiments prefer nanofabrics that are primarily a monolayer with sporadic bilayers and trilayers, but other embodiments benefit from thicker fabrics with multiple layers.

To create a nanofabric, the technique chosen must result in a sufficient quantity of nanotubes in contact with other nanotubes which thereby matte as a result of the nanotubes' adhesion characteristics. Certain embodiments (e.g., memory cells) benefit when the nanofabric is very thin (e.g., less than 2nm); for example, when the nanofabric is primarily a monolayer of nanotubes with sporadic overlapping (sometimes fabric will have portions that are bilayers or trilayers), or a multilayer fabric with relatively small diameter nanotubes. Moreover, many of these embodiments benefit when the nanotubes are single-walled nanotubes (SWNTs). Other embodiments (e.g., conductive traces) may benefit from thicker fabrics or multi-walled nanotubes (MWNTs).

The nanotubes have a resistance per square between 1-1000 $\text{k}\Omega/\square$ (a lower resistance per square value is generally preferred) but can be tuned to have a resistance per square between 1 $\text{k}\Omega/\square$ -10 $\text{M}\Omega/\square$ which is dependent upon the quality of nanotubes used and their electrical and mechanical characteristics. The porosity of the fabric can be tuned as well to generate low density fabrics with high porosity and high density fabrics with low porosity. The average length of a nanotube ranges between 50-1000nm and 1-100 μm including single-walled nanotubes, multi-walled nanotubes or a mixture of both and can be controlled as is necessary for a particular application such as memory, switches, relays, chemical sensors, biosensors and resonators.

Certain preferred methods of constructing the nanofabrics involve growing nanotubes using chemical vapor deposition (CVD) processes in conjunction with various catalysts. Other preferred methods generate films using spin-coating techniques with preformed nanotubes. The fabrics may be patterned after being formed or they may be grown or formed in a predetermined pattern, e.g., by using patterned catalyst metal layers, nanoparticles or a combination thereof.

Growing Nanofabrics

Introduction

Carbon nanotubes can be grown on substrates whose surfaces contain certain metallic or oxide layers. The metallic or metal oxide layers allow metal-containing nanoparticles to be applied onto the substrate surface. Exemplary nanoparticles include metals, such as iron, cobalt, nickel, tungsten, molybdenum, rhenium and other transition metals, or metal oxides. The metals or metal oxides in these methods act as growth catalyst for carbon nanotubes.

The literature has documented research results regarding the growth of single-walled nanotubes (SWNTs) from prefabricated nanoparticles (see Kong, J., et al., Chemical Physics Letters, 292, 567, 1998; Li, Y., et al., Journal of Physical Chemistry B, 105, 11424, 2001; Dai, H., et al., Journal of Physical Chemistry B, 103, 11246, 1999; Colomer, J.-F., et al., Chemical Physics Letters, 345, 11, 2001; and Li, Y. and Liu, J., Chem. Mater., 13, 1008, 2001), catalyst solutions, e.g., "liquid catalysts" (see Cassell, A., et al., Journal of Physical Chemistry B, 103, 6484, 1999 and Cassell, A., et al., Journal Am. Chem. Soc., 121, 7975, 1999), and layered catalytic deposition (see

Cassell, A., et al., Journal of Physical Chemistry B, 103, 6484, 1999). Metal oxides of various diameters may be used depending upon whether growth of single-walled nanotubes (SWNTs) or multi-walled nanotubes is desired. (See, e.g., Y. Li, W. Kim et al., Growth of Single-Walled Carbon Nanotubes From Discrete Catalytic Nanoparticles of Various Sizes, Journal of Physical Chem. B, 105, 11424, 22 Nov. 2001.) Bi-metallic catalyst nanoparticles (iron-molybdenum) have also been fabricated to assist in the production of carbon nanotubes (see Li, Y. and Liu, J., Chem. Mater., 13, 1008, 2001). These nanoparticles are usually dispersed randomly on a substrate or other support to produce nanotube growth. Typical liquid catalysts contain a mixture of chlorides or nitrates that have iron, cobalt, nickel, or molybdenum. These liquid catalysts are generated by soaking a pre-patterned 'stamp' onto a substrate. After stamping, the catalyst is calcinated or oxidized to burn off all the chlorides, nitrides, and other species leaving a random distribution of metal nanoparticles within a broad size regime. Yet another method of producing SWNTs involves the deposition of metal layers (see Delzeit, L., et al., Chemical Physics Letters, 348, 368, 2001). The metal layers may include a porous under-layer such as aluminum or iridium, a catalytic layer (iron, cobalt, nickel), and a co-catalyst layer, typically molybdenum. The catalyst nanoparticles required for nanotube formation are produced during the CVD process.

The inventors have discovered that the above techniques may be extended to create nanofabrics, which have important characteristics that may be controlled in the creation process. In addition, they have discovered new techniques to create nanofabrics. The fabrics can be assembled or grown (e.g., over an entire wafer surface) and then fabric may be selectively removed, e.g., by using lithographic patterning. In some embodiments, the fabric may be formed in a pattern; i.e., nanotube fabric will grow in places where desired and none need be removed subsequent to growth.

To grow nanofabrics, the metallic nanoparticles may be applied to the substrate surface in a variety of ways, including spin coating, application via aerosol, or by dipping the substrate into a solution that includes such nanoparticles. The metallic nanoparticles used as catalyst may also be applied to the substrate surface by deposition of a gas-phase metallic precursor such as any metallocene including ferrocene, molybdocene, cobaltocene and many other derivatives known in the literature to vaporize at relatively low temperatures, e.g. 25-600 °C (i.e., a low temperature relative

to the temperatures at which carbon nanotube growth would occur using that metal as catalyst).

Once a catalyst has been applied to the surface, an appropriate feedstock gas is supplied to the substrate's environment using a CVD process and nanotubes are allowed to grow. Typical growth times range from under 1 minute to 60 minutes. A typical growth phase can be completed in less than ten minutes. Examples of appropriate feedstock gasses include, but are not limited to CO, CH₄, C₂H₄ and other carbon sources. The feedstock gas should be used at proper flow rates and at proper concentrations with inert gasses such as argon or nitrogen. Typical temperature regimes are about 600-1000 °C.

Some factors influencing nanotube growth include catalyst composition, catalyst diameter, catalytic growth efficiency, temperature, CVD run time and choice of reagents including catalysts and feedstock gasses as well as reductants and inert carrier gasses, flow rates, ratios of gasses and mixtures and substrate type and composition.

The films generated by this method are typically characterized in bulk by resistance in ohms per square (Ω/\square) measurements that range from 1 to 1000 $k\Omega/\square$ or in some circumstances from 1 to 20 $M\Omega/\square$. This measurement can be used to describe the quality and density of the tubes in bulk growth where lower resistance per square indicates a denser fabric and a relatively high concentration of metallic nanotubes.

Thin Catalyst layers for Growth of Nanotubes

One preferred method of growing nanofabrics uses CVD techniques with substrates having a thin layer of metal catalyst on the substrate surface. The thin layers allow the catalyst to be easily removed in subsequent processing steps. Thicker catalyst layers may require more difficult processing steps.

Figure 1A shows a cross-sectional view of an exemplary structure 10 having a substrate 12 and a thin metal catalyst layer 14 (shown here as one layer, though more than one layer may be employed). This figure is not to scale; the metal catalyst layer of typical embodiments is only about 1-2 nm thick.

An exemplary, non-limiting substrate 12 is made of silicon and has an upper layer of SiO₂ (not shown). The SiO₂ insulates the conductive nanotubes (once formed)

from the underlying bulk silicon of substrate 12. Moreover, the upper layer of the substrate 12 may already have formed therein various elements that may be used together with the to-be-formed nanofabric articles to form circuits and the like, or the articles may be used as conductive connections between circuits formed on the substrate.

The metals that can be used as primary catalyst metals of layer 14 can be selected from a non-exclusive group known to produce SWNTs, such as iron, nickel, cobalt, and molybdenum. Metal layer 14 can also include metals that act in conjunction with primary catalysts as co-catalysts, such metals include, but are not limited to, aluminum, molybdenum, cobalt, or other co-catalyst metals. If multi-walled nanotubes (MWNTs) are desired, these and additional transition metals may be used in layer 14, such as yttrium, lanthanides and actinides.

The growth of nanotubes from deposited thin metal layers 14 typically involves the deposition by any physical vapor deposition technique of an aluminum layer, an iron layer, and/or a molybdenum layer, onto a substrate 12. (The aluminum layer generates a porous reactive support that aids in generation of carbon species which feed into the iron catalyst where growth of the nanotubes actually takes place. The molybdenum layer also serves as a site to reduce the carbon to a reactive form. The iron by itself can accomplish this reduction even but in some cases the rate is increased if the Mo and Al are present as well.) The thin metal layers 14 such as aluminum and molybdenum assist in the formation of SWNTs during CVD (when these three metals are used in concert, iron is the primary growth catalyst). These layers are extremely thin (e.g., 1-2 nm) and will diffuse or vaporize during CVD growth. Some of the particles created from such vaporization may be encapsulated by the eventually-grown nanotubes. (As the nanotubes are growing, the thin layers will diffuse. When layers are heated, they have a tendency to generate particles. Some of these particles will contain iron which will then be the site of the direct growth of carbon nanotubes. If in some instances the catalyst is very small, then the catalyst particle will be carried along as the nanotube grows. In other cases the catalyst particle will be larger and the nanotube will grow out from this end leaving the catalyst particle in place. Either way, if one looks at a transmission electron micrograph of a nanotube, one will almost always find at one end a nanoparticle, which acted as a catalyst.)

Figure 1B.1 illustrates a way of forming nanofabrics utilizing a substrate with thin metal catalyst layer(s). First, an intermediate structure 10 is provided 110. The structure, as outlined above, includes a substrate 12 and a metal catalyst layer 14. A furnace is heated 120 to about 500 °C. The structure 10 is placed 130 into the furnace. If desired, the metal layer 12 may be oxidized 140 in air. The oxidation can take place at 500 °C for 30 minutes. Oxidizing may be desired because it generates nanoparticles on the surface when metal atoms migrate and rearrange themselves. The temperature of the substrate 10 is ramped up to the CVD temperature and a flow of feedstock and inert gasses is provided 150. For example, hydrogen gas is mixed with an inert gas that has proper heat diffusion properties (typically argon or nitrogen). In some embodiments, the ratio of the gasses can be 1:5 hydrogen to inert gas (the ratio, however, should depend on the flow rate and types of gas introduced into the system upon reaching CVD temperature). For example, methane at a flow rate of 100-750 standard cubic centimeters per minute (sccm), or ethylene gas at 1.0-5.0 sccm may be used. The CVD run is performed 160 for some time typically between 1-10 minutes. (A CVD process or chemical vapor deposition involves in this case a carrier gas (argon), a reductant (hydrogen) and a carbon feedstock (methane, ethylene in combination or alone, or other gas)). The furnace is ramped down 170 to less than 200° C in a flow of inert gas or gasses that have low or no reactivity with the carbon sources, such as argon or nitrogen. Depending on properties desired in the resulting nanotube fabric, the gas used could be air or oxygen at a lower temperature; such use would provide a final annealing 180 for nanotube adhesion and/or removal of amorphous carbon. As a result of the above, a nanofabric is created over substrate 12 and the thin metal layer 14 is substantially or totally vaporized.

The surface of the substrate 12 may have a defined pattern (e.g., a grating) on its surface. For example, the surface may have alternating regions of metal or semiconductor and insulator. The metal or semiconducting embedded materials may be partially or totally capped off by a sacrificial layer which can be removed later to provide a suspended nanotube nanoribbon structure. See U.S. Pat. Apl. Ser. Nos. 09/915,093 and 10/033,323.

A defined thin metal layer pattern will determine the origination of nanotube growth. (That is, nanotube growth will originate from the catalyst regions rather than the

interstitial regions which do not have catalyst. This characteristic may be exploited; i.e., depending on the ultimate use of the nanoribbons or nanofabric articles, a specific surface pattern may be desired (for example in a memory device). Moreover, the thin metal layer catalyst may be patterned to create a patterned growth of nanofabric. If the catalyst patterns are sufficiently far away from each other they may not require subsequent patterning.

Figure 2, for example, is a cross-sectional view of an exemplary structure 15 having a grating configuration. Surface metal regions 17 are separated from one another by insulating regions 19. The material of metal regions 17 may be selected from appropriate carbon nanotube growth catalysts, and the insulating regions 19 may be made from material that does not readily initiate carbon nanotube growth and genesis, such as silica. The separate metal catalyst layer regions 17 provide a region where nanotube growth originates.

The density of the nanotube ribbon may be controlled by altering such variables as catalyst composition and concentration, growth environment, including but not limited to growth time (e.g., less CVD run time yields less dense nanofabric), temperature, gas composition and concentration. Provided below are several exemplary ways of growing nanofabrics using the above principles.

Example 1:

Thin metal layers of aluminum, iron, and molybdenum (15 nm, 1 nm, and 2 nm, respectively) are sequentially deposited on a substrate. The substrate is placed in a tube furnace in which the temperature is ramped to 500 °C and held for thirty minutes, in an ambience of air. The temperature is then ramped to a CVD temperature of 850 °C in a flow of argon gas and hydrogen gas, at 100: 400 sccm Ar: H₂. Upon reaching the CVD temperature, methane gas at a flow rate of 500 sccm is introduced into the furnace for a 1 minute growth time. Completing the CVD, the furnace is ramped down to below 200 °C in an argon atmosphere. Figure 1C is a micrograph of a fabric made from this procedure.

Example 2:

All parameters of example 1 are duplicated except in place of methane, ethylene is used at a flow rate of 5.0 sccm for 10 minutes, the CVD temperature is 800 °C. The

same types of metal layers are employed; however, the thicknesses of the metal layers used are 5 nm aluminum, 1 nm iron, and 2 nm molybdenum. Figure 1D is a micrograph of the nanotube growth resulting from such use of ethylene.

Examples 3-6:

Examples 3-6 show that the rate of methane gas flow affects the production of nanotube fabrics in typical CVD methods. From the micrographs one can see how the change in gas flow from 725 to 500 to 250 sccm affects the amount of growth. These examples show that the porosity and type of nanotubes grown may be controlled by changing specific parameters in the growth process. The growth of nanotubes is sustained over this range and can be finely controlled to generate primarily multilayer fabrics (750 sccm) to primarily monolayer fabrics (250 sccm). Reduction in gas flow to even lower levels is possible to assure primarily monolayer fabrics. An increase of the concentration would allow growth of fabrics with multilayers. Other parameters such as growth time and temperature can be controlled in concert with feedstock gas flow to provide more control over the fabric's characteristics.

Example 3: Methane is flowed at 725 sccm and the argon and hydrogen gas flow are kept constant at 100 sccm and 400 sccm, respectively. CVD is performed as above with the following parameters: the CVD is performed at 850 °C for 1 minute with the following metal layers: 15 nm aluminum, 1 nm iron and 2 nm molybdenum. Figure 1E is a micrograph of the film which resulted from this procedure.

Example 4: All parameters are kept the same as example 3 except methane gas flow is 500 sccm. Figure 1F is a micrograph of the film which resulted from this procedure.

Example 5: All parameters are kept the same as example 3 except methane gas flow is 250 sccm. Figure 1G is a micrograph of the film which resulted from this procedure.

Example 6: All parameters are kept the same as example 3 except methane gas flow is 100 sccm. Figure 1H is a micrograph of the film which resulted from this procedure.

Examples 7-9:

Examples 7-9 mirror examples 3-6 in that the flow rates of ethylene gas used are decreased in sequential CVD processes, while keeping all other variables constant. As above, all of these examples show fine control may be achieved over the growth density, nanotube porosity, nanotube length and the resistance per square values. (Resistance per square is used to assess in general the porosity of the nanotubes and/or their overall conductive quality.) Figures for examples 7-9, respectively, show fabrics corresponding to decreasing the gas flow. As flow decreases, the fabric density decreases and resistance increases.

Example 7: The argon flow and hydrogen flow are kept constant at 100 sccm and 400 sccm, respectively. Ethylene gas is flowed at 5.0 sccm. Metal layers are as follow: 5.0 nm aluminum, 1.0 nm iron and 2.0 nm molybdenum, the CVD temperature is 800 °C, and is run for 10 minutes. Figure 1I is a micrograph of the film, which resulted from this procedure.

Example 8: All parameters are kept the same as example 7 except ethylene gas flow is 2.5 sccm. Figure 1J is a micrograph of the film which resulted from this procedure.

Example 9: All parameters are kept the same as example 7 except ethylene gas flow is 1.0 sccm. Figure 1K is a micrograph of the film which resulted from this procedure.

Examples 10-12:

Examples 10-12 show the effects of reducing CVD temperatures while keeping all other parameters constant. The CVD methods are otherwise much the same as in example 1. These examples also show that fine control may be achieved in porosity, thickness and length of nanofabrics and nanotubes. Figures for examples 10-12, respectively, show fabrics corresponding to decreasing CVD temperatures. As the temperature decreases, the fabric density decreases, and the resistance increases.

Example 10: CVD is performed on a substrate of silicon coated with 15 nm aluminum, 1 nm iron and 2 nm molybdenum, using a 725 sccm flow of methane gas at 900 °C for ten minutes in Ar/H flow as above. Figure 1L is a micrograph of the film which resulted from this procedure.

Example 11: All parameters are kept the same as in example 10, except the CVD temperature is decreased to 850 °C. Figure 1M is a micrograph of the film which resulted from this procedure.

Example 12: All parameters are kept the same as in example 10, except the CVD temperature is decreased to 800 °C. Figure 1N is a micrograph of the film which resulted from this procedure.

Examples 13-16:

Figures for examples 13-16, respectively, show fabrics corresponding to decreasing CVD run time. As the run time decreases, the fabric density decreases, and the resistance increases.

Example 13: CVD is run for 10 minutes on a substrate of silicon coated with 15 nm aluminum, 1 nm iron, and 2 nm molybdenum at 850 °C in a 725 sccm flow of methane gas and 100:400 sccm Ar:H₂ as above. Figure 1O is a micrograph of the film which resulted from this procedure.

Example 14: All parameters are kept the same as example 13, except the CVD run time is decreased to five minutes. Figure 1P is a micrograph of the film which resulted from this procedure.

Example 15: All parameters are kept the same as in example 13, except the CVD run time is decreased to two minutes. Figure 1Q is a micrograph of the film which resulted from this procedure.

Example 16: All parameters are kept the same as in example 13, except the CVD run time is decreased to one minute. Figure 1R is a micrograph of the film which resulted from this procedure.

Examples 17-20:

Examples 17-20 show the effect that varying the thicknesses of the aluminum metal layer has on the resulting films. As above, all of these examples show fine control may be achieved over the growth density, nanotube porosity, nanotube length and the resistance per square values. Figures for examples 17-20, respectively, show fabrics

corresponding to decreasing thickness of the metal layer catalyst. As the thickness decreases, the fabric density decreases, and the resistance increases.

Example 17: CVD is performed on a substrate of silicon coated with 25 nm aluminum, 1 nm iron and 2 nm molybdenum, using a 725 sccm flow of methane gas and the argon and hydrogen gas flow are kept constant at 100 sccm and 400 sccm, respectively, at 850 °C for ten minutes. Figure 1S is a micrograph of the film which resulted from this procedure.

Example 18: All parameters are kept the same as in example 17, except the thickness of the aluminum layer is decreased to 15 nm. Figure 1T is a micrograph of the film which resulted from this procedure.

Example 19: All parameters are kept the same as in example 17, except the thickness of the aluminum layer is decreased to 5 nm. Figure 1U is a micrograph of the film which resulted from this procedure.

Example 20: All parameters are kept the same as in example 17, except the thickness of the aluminum layer is decreased to 0 nm (no aluminum is deposited in this example). Figure 1V is a micrograph of the film which resulted from this procedure.

Examples 21-22:

Examples 21-22 show the results of altering thin metal layer thickness and using silicon dioxide as substrate. Altering the metal layer thickness allows tuning of the porosity and specifically the type of nanotubes. Thicker layers are more conducive to growing MWNTs while thinner layers grow better SWNTs and leave less residual metal because they are vaporized at the high temperatures of nanotube growth. Figures for examples 21-22, respectively, show fabrics corresponding to decreasing thickness of the metal layer catalyst. As the thickness decreases, the fabric density decreases, and the resistance increases.

Example 21: CVD is performed on a silicon dioxide substrate coated with thin metal layers; 2.0 nm aluminum, 0.5 nm iron and 1.0 nm molybdenum at 850 °C in a 500 sccm flow of methane gas in 100:400 sccm Ar:H₂ for one minute. Figure 1W is a micrograph of the film which resulted from this procedure.

Example 22: All parameters are kept the same as example 21, except thin metal layers of the following thicknesses; 1.0 nm aluminum, 0.5 nm iron and 1.0 nm molybdenum were used. Figure 1X is a micrograph of the film which resulted from this procedure.

Examples 23-24:

Examples 23 and 24 show the films that are grown by CVD on silicon and silicon dioxide substrates. These examples illustrate control over porosity even on different substrates. Here we have an example of a semiconducting substrate and an insulating substrate. Growth is achievable on a variety of substrate layers allowing ready integration into typical semiconductor process flows and ease of manufacture. Figures for example 23 and 24 show that the fabric density changes with the type of substrate, and thus resistance changes.

Example 23: CVD is performed on a silicon substrate coated with thin metal layers; 15 nm aluminum, 1.0 nm iron and 2.0 nm molybdenum at 850 °C in a 500 sccm flow of methane gas for two minutes. Figure 1Y is a micrograph of the film which resulted from this procedure.

Example 24: All parameters are kept the same as example 23, except silicon dioxide is used as substrate. Figure 1Z is a micrograph of the film which resulted from this procedure.

Growing Nanofabrics with Nanoparticles

Another preferred method of growing nanofabrics uses metallic or metal oxide nanoparticles (e.g., iron oxide) as carbon nanotube growth catalyst. Metallic or metal-oxide nanoparticles have a narrow range of diameters. This narrow range can lead to more effective control over the diameter and type of nanotubes forming the eventual nanofabric. The surface of the substrate used can be derivitized to create a more hydrophobic or hydrophilic environment to promote better adhesion of the catalyst particles. The nature of the substrate allows control over the level of dispersion of the nanoparticles to a precision sufficient to generate monolayer nanotube fabrics.

Figure 3A shows a cross-sectional view of an exemplary structure 20 used to grow nanofabrics. A substrate 12 has a distribution 16 of metallic or metal oxide

nanoparticles thereon. (For simplicity, the figure shows the distribution as a continuous layer, though people skilled in the art will appreciate that in reality the structure 20 will have a distribution of relatively discrete nanoparticles.) The substrate surface used for generation of carbon nanotubes may be any material including, but not limited to, silicon, thermal oxide, silicon oxide, silicon nitride, tungsten, tungsten/titanium and other typical insulators, semiconductors and metallic surfaces commonly used in CMOS and semiconductor fabrication processes the surface may have electronic components and patterns already defined therein, as mentioned above, and the substrate may be functionalized or non-functionalized.

Figure 3B illustrates a way of growing a nanofabric using a substrate coated with nanoparticles 16. A mixture of ferritin and water is created. For example, ferritin dissolved in deionized (DI) water at a typical concentration (1-1000 μM) (SIGMA catalog) is provided. The ferritin contains naturally encapsulated iron in an organic layer or shell, and can be processed so that the encapsulated iron may be used in subsequent nanotube generation. This shell is oxidized using air or oxygen oxidation or plasma ashing, which causes its removal leaving only an iron oxide nanoparticle. During CVD growth of nanotubes the iron oxide nanoparticles are reduced to leave metallic iron nanoparticles which catalyze the growth of nanotubes. The purpose of using ferritin or any appropriate nanoparticles is to cause the nanoparticles to be dispersed on the surface in an even fashion (monodisperse). Ferritin particles have a very narrow diameter range as do the nanoparticles discussed below.

The ferritin solution is applied 310 to a surface of substrate 12. Before application the substrate can be derivitized to make it more hydrophilic or hydrophobic in order to promote adhesion of the ferritin to the surface. The substrate is allowed to dry 320 (e.g. approximately five minutes has been found to be sufficient). This leaves a coating of ferritin on the surface of the substrate. The protein shells are then removed 330 from the ferritin particles. For example, the structure may be subjected to either an oxidation operation at 400-800 $^{\circ}\text{C}$ for about 15 minutes, or it may be subjected to a plasma ashing operation. The oxidation process removes substantially all of the proteinaceous shell from the ferritin, thereby leaving behind a coating 16 of nanoparticles of iron oxide. The nanoparticles are approximately two to five nanometers in diameter, or more particularly approximately three nanometers in

diameter. (See Li, 46 Journal Physical Chem. above.) Once catalyst particles from ferritin are formed, CVD may be performed 340 to grow a nanofabric of nanotubes. The nanofabric may be grown, for example, over an entire wafer surface as a monolayer of contacting nanotubes. The above embodiment is conducive to growing a conductive (primarily) monolayer fabric with sufficient density to remain suspended over a switching junction.

Under yet other embodiments, metal ligand-catalyst precursor molecules are used to deposit metallic nanoparticles on a functionalized substrate surface to thereby help create growth of nanotubes. Typically, the formula of the metal/ligand complex will have a formula such as ML, in which M is a metal such as iron, cobalt, or nickel, and L is one or more organic ligands having an affinity for the metal. One general formula may be $C_xH_y(COOH)$, but other carbon, oxygen, nitrogen and/or sulfur-containing ligands are known and may be used. A metallic nanoparticle ligated to an organic moiety is deposited on a functionalized substrate surface. The surface is functionalized to optimize ligand bonding during spin coating, a procedure which may result in minimal deposition of untreated nanoparticles. Certain embodiments use a generic method to synthesize metallic nanoparticles with organic shells which have a very specific size regime for example 3-5 nm which can be monodisperse on a substrate.

Certain embodiments use prefabricated iron oxide particles as carbon nanotube growth catalyst. Iron oxide nanoparticles are applied to a substrate in a concentration sufficient to support the desired density of nanotube growth. The substrate is then subjected to a CVD operation as described herein. The substrate, optionally, can be dried and/or oxidized prior to beginning the CVD run. For example, iron oxide nanoparticles may be applied to a substrate surface by spin coating. In one embodiment, iron oxide is suspended in deionized water at a 1:10 ratio. The aqueous iron suspension is applied to a substrate surface, and the surface is spun at approximately 1000 rpm to distribute the suspension. The surface is then spun at 4000 rpm to dry the suspension. More than one application of iron oxide nanoparticles may be performed. The number of applications of iron oxide nanoparticles required will vary depending on the concentration of the suspension used, the desired resulting surface density of nanoparticles, the physical properties of the desired fabric, and the physical properties of the substrate used.

Under yet other embodiments, a liquid catalyst precursor suspension is used. Figure 3C illustrates a way of growing a nanofabric using liquid metal catalyst. A liquid metal catalyst is created. For example, a dissolved metal catalyst, e.g., iron nitrate ($\text{Fe}(\text{NO}_3)_3$), is mixed with methanol and applied onto a substrate 350. The substrate is oxidized 360, e.g., by ashing, thereby leaving a dispersion of iron oxide nanoparticles on the surface of the substrate. The substrate is then subjected to a CVD operation 370 to grow nanotubes. Provided below are several exemplary ways of growing nanofabrics using the above principles.

Example 25:

This is an example of nanoparticles using metal-ligand catalyst precursor molecules. HMDS (hexamethyldisilane) is spun onto a silicon dioxide substrate at 4000 rpm for one minute as an adhesion layer. Iron nanoparticles are made by dissolving $\text{Fe}(\text{NO}_3)_3$ in solution of a lauric acid in methanol at a ratio of 1:3.3 Fe: lauric acid. The nitrate solution is evacuated to pump off nitric acid, and the solvent. The dried iron nanoparticles are then added to 10 mL toluene and 10 mL isopropanol to resuspend the nanoparticles in solution. The Fe nanoparticle solution is then diluted 1:25 in isopropanol. The iron nanoparticles in a 1:25 iron nanoparticle solution in isopropanol is then deposited on the wafer by spinning at 1000 rpm for 30 seconds, then at 4000 rpm for 20 seconds. Two Fe nanoparticle applications are deposited and spun. The substrate is baked at 100 °C to remove the solvent, it is then ashed for 30 minutes in O_2 plasma, CVD is performed at 850 °C for ten minutes in a 500 sccm flow of methane and a 100:400 sccm flow of Ar:H_2 . Figure 3D is a micrograph of a nanofabric which resulted from this procedure. The nanoparticles in this embodiment can be tuned to a particular size by changing the organic ligand (analogous to the protein shell of ferritin) that is bound to the metal. Additionally, nanoparticles of different metal or metal oxide species may be mixed together in a solution and applied for use as catalyst, e.g., 50% Fe and 50% Co, or 33% Fe 33% Co and 33% Al, or any other appropriate combinations.

Example 26:

This is an example of iron nanoparticles in solution which are dispersed onto a silicon dioxide substrate and not spin coated on the surface. After the catalyst is dispersed onto the surface, the substrate is allowed to stand for 5 min., covered, and baked at 100 °C to remove solvent, and it is ashed. CVD is performed at 850 °C for ten

minutes in a 500 sccm flow of methane and a 100:400 sccm flow of Ar: H₂. Figure 3E is a micrograph of a nanofabric which resulted from this procedure.

Example 27:

Example 27 demonstrates the growth of carbon nanotubes from a substrate with ferritin on the surface. The process involves the use of ferritin as a catalyst precursor. A 1:10 mixture of ferritin in deionized water is applied to a silica surface of a wafer. The wafer is dried, leaving a dispersed coating of ferritin on the surface of the substrate. The substrate is oxidized to remove all non-iron, organic matter and placed in the oven. The oven is ramped to 700 °C for 10 minutes in Ar:H₂, then it is ramped to 800 °C for seven minutes in Ar:H₂. CVD is performed at 800 °C with a 10 sccm flow of ethylene for 40 minutes in 600:400 sccm Ar:H₂. Figure 3F shows a FESEM micrograph of carbon nanotubes grown using ferritin as catalyst precursor.

Growing Nanofabrics with a combination of nanoparticles and thin metal layers

Another preferred method of growing nanofabrics uses nanoparticles in conjunction with thin metal layers on a substrate surface. This method allows one to easily distribute catalyst particles while taking advantage of the ease of thin layer deposition and their properties in assisting the process of growing nanotubes. Recall that aluminum and molybdenum are useful in generating surface carbon precursors that grow nanotubes.

Figure 4A shows a cross-sectional view of an exemplary structure 30 used to grow nanofabrics. A substrate 12 has a thin layer 14 of metal catalyst and a distribution 16 of nanoparticles thereover. The substrate surface used for generation of carbon nanotubes may be any material including, but not limited to silicon or thermal oxide, e.g. silicon oxide, alumina. The uppermost layer can be an insulator, semiconductor or metal. Typical substrates which are of interest include silicon dioxide (SiO₂), silicon nitride (Si₃N₄), titanium, titanium/tungsten and others used in standard CMOS and semiconductor processing. The surface may have already formed therein various components and structures (e.g., gratings) of the aforementioned materials. In addition, the surface may be functionalized or non-functionalized.

Figure 4B illustrates a way of growing a nanofabric of carbon nanotubes (e.g., to cover a wafer surface) by using nanoparticles in conjunction with thin metal layers.

First, a substrate 12 is provided and a thin layer of metal catalyst is provided 410 to at least selected regions of a wafer or perhaps an entire wafer surface, as described above. This forms layer 14 of metal catalyst. Thereafter, a distribution of nanoparticles 16 is applied 420 to the surface of layer 14. This may be done using any of the above methods of applying nanoparticles, e.g., spin coating suspensions of nanoparticles. Catalyst precursors such as ferritin, liquid metal catalyst precursor and metal ligand-catalyst precursor molecules may also be used in conjunction with thin metal layers on substrates to grow carbon nanotube fabrics. Depending on how the nanotubes are applied, the substrate may be dried (optionally) 425. The substrate is oxidized 430. Once so formed, the structure 30 may be subjected to a CVD process 440 to form a nanofabric.

Forming Nanofabrics With Pre-Formed Nanotubes

Introduction

One preferred method of forming a nanofabric uses spin coating techniques in conjunction with pre-formed nanotubes. Nanotubes should be sufficiently free of amorphous carbon if the nanotubes are to be used as electronic elements. Among other advantages, this technique is more conducive to semiconductor manufacturing environments than growth of nanotubes by CVD because it uses a room temperature process that does not contribute to the thermal budget of the standard CMOS process flows or semiconductor manufacturing methods. Additionally, the overall cost of this integration of nanotubes is very inexpensive.

Figure 5A shows an exemplary structure 50 having a wafer substrate 12 and a nanofabric 54 thereover. The nanofabric 54 may be made to cover an entire wafer surface.

An exemplary, non-limiting substrate 12 is like those described above. The substrate may be any material that will accept the deposition of nanotubes by spin coating, but preferably a material chosen from the group consisting of a thermal oxide or nitride, including but not limited to silicon dioxide, silicon nitride, alumina on silicon, or any combination of the following on silicon or silicon dioxide: aluminum, molybdenum, iron, titanium, platinum, and aluminum oxide, or any other substrate useful in the semiconductor industry.

Spin Coating Nanotubes on Functionalized Substrate Surfaces

Figure 5B shows a way of making a fabric of nanotubes on a functionalized carbon nanotube growth substrate surface 52. The substrate surface 52 may be prepared for spin coating by functionalizing the surface. Specifically, functionalization of a wafer/substrate surface involves derivitizing the surface of the substrate. For example, one could chemically convert a hydrophilic to hydrophobic state or provide functional groups such as amines, carboxylic acids, thiols or sulphonates to alter the surface characteristics of the substrate. Functionalization may include the optional primary step 510 of oxidizing or ashing the substrate in oxygen plasma to remove carbon and other impurities from the substrate surface and to provide a uniformly reactive, oxidized surface which is then reacted with a silane. One such polymer that may be used is 3-aminopropyltriethoxysilane (APTS). The substrate surface 52 may be derivitized 520 prior to application of a nanotube suspension to enhance bonding of the nanotubes. The inventors foresee that any reactive silane could be used in functionalization of such a surface. In a particular, non-limiting embodiment, the substrate surface 52, whether or not subjected to ashing, is exposed to an approximately 1 to 50 millimolar solution of APTS in suitable organic solvent, e.g. hexane, but more preferably 13 to 28 millimolar APTS in hexane, such that approximately a monolayer of APTS is deposited on the substrate surface. In order to form such a monolayer of APTS, the substrate typically is immersed in an APTS solution for 30 minutes. Once the surface 52 is prepared for spin coating, carbon nanotubes are dispersed 530 on the surface, and the surface is subjected to spinning in order to disperse the nanotubes, forming a nanotube fabric (e.g., fabric 54 of Fig. 5A). The substrate is then (optionally) annealed 540.

Different methods may be employed to apply nanotubes to surfaces to form nanofabrics: to attain desired fabric properties; the selection of one method over another depends, in part, on the properties of the pre-formed nanotubes being used. For example, under certain embodiments laser-ablated SWNTs are used; under other embodiments, commercially-available high pressure carbon monoxide decomposition SWNTs nanotubes are used, such as HiPcoTM nanotubes available from Rice University; under still other embodiments, other nanotubes may be used.

Under some embodiments, laser-ablated nanotubes are mixed with a solvent at a concentration of about 100-500 µg/mL. Solvents which are quite useful for suspension

of SWNTs and distribution via spin coating include isopropanol, methanol, ethanol, 1,2 dichlorobenzene, 1,3 dichlorobenzene, 1,4 dichlorobenzene, chlorobenzene, n-methylpyrrolidinone, dimethylformamide, dimethylsulfoxide, acetonitrile, hexane, toluene, methylene chloride and chloroform. While all of these solvents have the ability to suspend nanotubes, the precise characteristics of the film desired and substrate used are important for solvent selection. If a low boiling solvent is desired hexane would, for example, be a better selection than DMSO. 1,2 dichlorobenzene is a preferred solvent owing to its excellent suspension properties and compatibility with industrial semiconductor processes.

Under some embodiments, HiPco™ nanotubes may be used. The HiPco™ nanotubes are SWNTs and relatively free from amorphous deposits, fibrous deposits and other impurities. HiPco™ tubes are mixed into orthodichlorobenzene at a more dilute concentration than are laser ablated nanotubes, typically 10-200 µg/mL.

Under the above embodiments, the preferred concentration depends on the length of the nanotubes used. Laser ablated nanotubes tend to have overall greater lengths than HiPco™ tubes. Regardless of the nanotubes used, the nanotubes in mixture should be adequately dispersed, e.g., by sonication.

Adequately-dispersed nanotubes may be applied 530 onto a substrate surface by spin coating. Such a surface should be relatively free of any residue remaining after storage or after any substrate preparation step, e.g. functionalization of the surface. If solvent, e.g. hexane is present on the substrate's surface, it may be removed, e.g., by baking at 100-115 °C for 1 minute. After removal of any storage solvent, the nanotubes are spun onto the substrate surface.

One way of spin coating the nanotubes involves spinning the substrate, for example, at 1000 rpm while depositing the nanotube solution on the substrate surface, for about 30 seconds or alternatively they can be applied before the spinning has begun. The substrate may (i.e., optionally) then be dried, for example, by spinning at 4000 rpm until dry. Further coats of nanotube suspension may be applied in like manner, until the substrate surface is coated with the desired density of nanotubes. Ribbon density may be varied based on desired use. Adequate layers of nanotubes typically have resistance per square measurements between 1-1000 kΩ/□. For particular applications, nanotube

layers with resistances per square below $1 \text{ k}\Omega/\square$ may be preferred, and for yet other uses, nanotube films with resistance per square measurements of $1\text{-}10 \text{ M}\Omega/\square$ may be sufficient. Typically four coatings of the nanotube suspension are applied to the substrate surface to create a fabric that will have a redundancy of electrically conductive pathways. After a layer of nanotubes of desired density, i.e., a monolayer, is spun onto the substrate, the substrate may be baked once again to remove any remaining solvent, e.g., at $100\text{-}115^\circ\text{C}$. After four coatings are applied as described, a fabric resistance per square of $\sim 100 \text{ k}\Omega$ is typically measured. The actual resistance per square depends upon the quality of the nanotubes used, their compositions, and overall purity.

Spin Coating Nanotubes on Non-Functionalized Substrate Surfaces

A non-functionalized substrate surface may be coated with nanotubes by spin coating. The surface may be oxidized, e.g., by ashing in oxygen plasma, to remove surface impurities, or it may be coated and not oxidized. The nanotubes used may be, but are not limited to, laser-ablated SWNTs or HiPcoTM nanotubes.

Adequately dispersed nanotubes may be deposited on a non-functionalized substrate surface by spin coating. Similarly to the above, the substrate may be spun at 1000 rpm for 30 seconds while applying a nanotube solution to the substrate surface to distribute the nanotubes or the solution may be applied first and then spun. Further coats of nanotube suspension may be applied until the substrate surface is coated with the desired density of nanotubes. The substrate may be dried (optionally) between application steps, e.g., by spinning at 4000 rpm until dry.

Similarly to the above, ribbon density may be varied based on desired use. Typically, eight coatings of the nanotube suspension are applied to the non-functionalized substrate surface, when using the preceding parameters, to attain a fabric of electrically conductive nanotubes. After a layer of nanotubes of desired density is spun onto the substrate surface, the substrate can be baked once again to remove any remaining solvent, e.g. at $100\text{-}115^\circ\text{C}$. Such a method typically results in a nanotube layer resistance per square measurement of $\sim 1\text{-}100 \text{ k}\Omega$ which is dependent both on the number of applications performed and the purity and character of the nanotubes used. Because nanotubes that have been deposited on a surface may be solvated and removed by subsequent applications of nanotubes in solvent, it may be desirable to cure the substrate and nanotubes before subsequent applications of solvated nanotubes. Such

curing may be accomplished through evaporation or drying. Another way of limiting the subsequent dissolution and removal of already-spun-on tubes (removal by dissolution and from the centrifugal force overcoming van der Waals attraction between the nanotubes and the substrate surface) is to use a different solvent for subsequent spin coating steps.

The density of the nanotube ribbon may be controlled by altering such variables as including but not limited to functionalization of the underlying surface, spin coating parameters (length of time and RPM), solvent choice, nanotube type and concentration, diameter and length of nanotubes, number of applications and substrate type and composition.

Provided below are several exemplary ways of forming nanofabrics using the above principles.

Example 28:

A wafer substrate is first ashed in oxygen plasma for 15 minutes. After ashing, the substrate is bathed for 30 minutes in a solution of 3-aminopropyltriethoxysilane (APTS), the functionalization agent, and hexane at a ratio of 30-60 μ L of APTS to 10 mL of Hexane. During the surface functionalization step, a nanotube solution is prepared. HiPco™ SWNTs are mixed in a solution comprising 1 mg of nanotubes and 50 ml 1,2 dichlorobenzene. The nanotube solution is then sonicated for 1 hour to adequately disperse the nanotubes in the solvent solution. Before nanotube deposition, the substrate is removed from the hexane bath and is baked at 100-115 °C for 1 minute to remove any solvent residue. After baking, the nanotubes are spun onto the wafer at 1000 rpm for 30 seconds to distribute the nanotubes, and then they are spun at 4000 rpm to dry the wafer. Four such SWNT spin coatings are applied to the wafer. After spinning, the wafer is baked again at 100-115 °C to remove any remaining solvent.

A resistance per square measurement of 1-100 k Ω was measured. Figures 5C-D display FESEM images of different magnifications of HiPco™ SWNTs spun onto a functionalized surface.

Example 29:

All parameters are kept the same as in example 28 except 10 mg of laser-ablated nanotubes are mixed in 100 mL of 1,2 dichlorobenzene and are spun onto a wafer surface. A resistance per square of 100-400 k Ω was measured. Figure 5E displays a FESEM image of spun-on laser-ablated SWNTs with a functionalized surface. Some particles containing amorphous carbon impurities are observed also.

Example 30:

All parameters are kept constant as in example 29, except the substrate used for spin coating was stepped, i.e., not horizontally planar. Figure 5F displays a micrograph of a nanofabric spun on to the substrate according to this method; the micrograph shows that nanotubes conform to a substrate surface via van der Waals attraction. The inventors contemplate that conformal nanofabrics may be useful in fabrication of non-horizontal electromechanical switches, especially vertical electromechanical switches or also as interconnects, actuators, relays, sensors and other electronic elements.

Example 31:

Carbon nanotubes are deposited on a non-functionalized surface as follows. A wafer surface is ashed for 1 minute. A nanotube solution is deposited and spun on to the wafer as presented in Example 28, above. Eight applications of nanotube mixture are applied to the wafer surface, producing resistance per square measurements on varying sections of the nanotube fabric ranging from 50 to 100 k Ω . Figure 5G displays an FESEM image of SWNTs spun onto a non-functionalized wafer surface with sufficient applications to generate a multilayer nanofabric. Figure 5H displays an FESEM micrograph of a monolayer fabric spun onto a substrate which has a prefabricated metal electrode with a width of about 130 nm shown.

Preferred embodiments operate with a range of concentrations for pre-formed nanotubes. For example for laser ablated nanotubes a concentration of about 0.1-5 mg/mL (100-500ug/mL) is used. The concentration is preferably adjusted depending upon the purity and length of the nanotubes; for example, shorter nanotubes have one spinning regime and longer ones have a different regime.

In addition, preferred embodiments preferably subject the nanotube solution to sonication. For example, preferred embodiments use sonication times such as 30-120 minutes.

Patterning Nanofabrics

The new and improved methods for creating nanofabrics may be used to create articles therefrom. The U.S. Patent Applications, identified and incorporated above, describe specific (but not limiting) uses of such fabrics and articles. For example, the various masking and patterning techniques for selectively removing portions of the fabric are described in these applications but are not repeated here for the sake of brevity. Moreover, various component architectures are described in the incorporated applications but not repeated here for the sake of brevity.

Figure 6, for example, is a cross-sectional view of exemplary structures used in creating patterned nanofabrics. This method creates patches of carbon nanotube fabric which can be used as electronic elements. Such a patch of nanotube fabric may be used as an electromechanical switch, or as an electronic interconnect. An intermediate structure 600 is provided. Structure 600 comprises a nanofabric 620 overlying a substrate 610. The substrate 610 could be a simple substrate made of a single material; it could be a substrate which has already undergone some processing, e.g., to include vias, plugs or other elements, etc. The nanofabric 620 may be grown or formed using any of the methods disclosed or incorporated above. The nanofabric may be of SWNTs or multi-walled nanotubes. A layer of resist 630 is applied over the nanofabric 620 to form intermediate structure 640. The resist 630 is then patterned using any of a variety of techniques, including but not limited to those described in the incorporated references. For example, the resist may be patterned to correspond to the desired pattern of nanofabric patches, so that the resist will cover (and define) the desired patches. Removing selected portions of the resist (e.g., exposed portions) will create intermediate structure 650. The intermediate structure 650 includes exposed nanofabric portions 670 and remaining resist portions 660. The exposed nanofabric portions 670 may be removed in many ways; for example, by performing a reactive ion etch procedure, or oxidizing the substrate, by plasma ashing, air oxidation or other reaction methods to remove all nanotube fabric except for the desired patches, thereby creating intermediate structure 680. The remaining resist portions 660 may then be stripped from

intermediate structure 680, yielding structure 690 which includes patterned patches 695 of nanofabric.

As explained in the incorporated references, the nanofabric 620 may be formed or grown over defined regions of sacrificial material and over defined support regions. The sacrificial material may be subsequently removed, yielding suspended articles of nanofabric. See, for example, Electromechanical Memory Array Using Nanotube Ribbons and Method for Making Same (U.S. Pat. Apl. Ser. No. 09/915,093) filed July 25, 2001, for an architecture which suspends ribbons of nanofabric.

Figure 7, for example, is a cross-sectional view of exemplary structures used in creating suspended, patterned nanofabrics. This method creates suspended patches of carbon nanotube fabric, which can be used as electronic elements. Such a patch of nanotube fabric may be used as an electromechanical switch, or as an actuator, a relay, a sensor, especially a biosensor or chemical sensor.

An intermediate structure 700 is provided. Structure 700 comprises defined regions of sacrificial material 720 overlying a substrate 710 (which as outlined above could made of a single material; could be a substrate which has already undergone some processing, e.g. to include vias, plugs or other elements, etc.). A nanofabric 730 covers the substrate surface and the sacrificial material 720. The nanofabric 730 may be formed or grown as outlined above and may be multilayer or single layer and may have single- or multi-walled nanotubes. A layer of resist 740 is applied over the nanofabric 730 to create intermediate structure 745. The resist 740 is then patterned (not shown). Removing selected portions of the resist (e.g., exposed portions) will create intermediate structure 750. The intermediate structure 750 includes exposed nanofabric portions 770 and remaining resist portions 760. The exposed nanofabric portions 770 may be removed in many ways; for example, by performing a reactive ion etch procedure, or oxidizing the substrate, by plasma ashing, air oxidation or other reactive methods to remove all nanotube fabric except for the desired patches, thereby creating intermediate structure 780. The remaining resist portions 760 may then be stripped from intermediate structure 780, yielding structure 790 which includes patterned nanofabric patches 795 overlying defined sacrificial material 720. The sacrificial layer 720 is removed by selective etching, leaving substantially intact the suspended patterned nanofabric 795 and leaving an air gap 798 in the place of the removed sacrificial layer.

The inventors contemplate that the stripping of the remaining resist portions 760 and removal of sacrificial material 720 may be done in the same step, with an appropriate process.

Figure 8A, for example, is a cross-sectional view of exemplary structures used in creating suspended, patterned nanofabrics. This method creates suspended patches of carbon nanotube fabric overlying an electrode with which the nanofabric may come into electrically conductive contact when the nanofabric is deflected. Such a device can be used as an electronic element, e.g. as an electromechanical switch, etc.

An intermediate structure 800 is provided. Structure 800 comprises a substrate 810 (similar to those described above) with already defined electrodes 820 (e.g., made of sufficiently conductive material, such as doped semiconductor or metal) and defined sacrificial material 830 thereover. A nanofabric 840 covers the substrate surface and the sacrificial layer 830. The nanofabric may be made by any of the above-described methods. Similar to that described above and as described in the incorporated references, the nanofabric 840 may be patterned (e.g., lithographic patterning) and defined portions of nanofabric may be removed to form intermediate structure 850. Patterned nanofabric articles 860 then cover defined sacrificial material 830 which in turn cover electrodes 820. The sacrificial material 830 may then be removed by selective etching, leaving remaining structures substantially intact, yielding structure 870. Structure 870 comprises suspended nanofabric articles 860 separated from electrodes 820. The nanofabric articles 860 and/or the electrodes may then be subjected to electrical stimulus to cause the nanofabric articles 860 to deflect toward, or away from, electrodes 820. As described in the incorporated references, the deflected articles retain their deflected state in a non-volatile manner.

Example 32:

A wafer substrate, an overlying nanofabric, an embedded titanium electrode under a sacrificial layer of Al_2O_3 are provided. Shipley 1805 photoresist is applied to the wafer surface by spin coating at 4000 rpm for 60 seconds. The photoresist is exposed using a Kasper Mask Aligner for 8 seconds. The pattern is developed using a basic developer, thereby exposing portions of nanofabric and leaving other portions protected by the photoresist. The substrate is rinsed in deionized water and dried at

115 °C. The exposed portions of nanofabric are removed by plasma ashing for five minutes with 25 cubic feet per minute oxygen at a pressure of 280 millitorr and a power of 300 Watts. The substrate is soaked in n-methylpyrrolidinone at 70 °C to remove remaining photoresist for 30 minutes. The substrate is rinsed in isopropanol and dried. Hot phosphoric acid is applied to remove the Al_2O_3 , leaving a patterned nanofabric suspended over an electrode with which it may come into electrical contact when deflected. Figure 8B displays an FESEM image of patterned nanofabrics made by this method. In the micrograph, bare substrate regions are dark, nanofabric patches are light in color and the longitudinal light stripe is a metallic electrode. Typical resistivity for a patterned trace with a length of 100 μm and width of 3 μm is 1-10 M Ω . Figure 8C displays an FESM image of the same structure as in 8B under greater magnification. The dark longitudinal stripe is the sacrificial layer overlying the metal electrode. Figure 8D displays an FESM image of the same structure with the sacrificial layer removed; the nanofabric can be seen suspended over and not in contact with the electrode.

Controlled Composition of Types of Nanotubes in Nanofabric

Other embodiments involve controlled composition of carbon nanotube fabrics. Specifically, methods may be employed to control the relative amount of metallic and semiconducting nanotubes in the nanofabric. In this fashion, the nanofabric may be made to have a higher or lower percentage of metallic nanotubes relative to semiconducting nanotubes. Correspondingly, other properties of the nanofabric (e.g., resistance) will change. The control may be accomplished by direct growth, removal of undesired species, or application of purified nanotubes.

With regard to controlled direct growth, methods are known, for example, to selectively grow semiconducting nanotubes. (See Kim et al., Synthesis of Ultralong and High Percentage of Semiconducting Single-Walled Carbon Nanotubes, Vol. 2 Nanoletters 703 (2002).) The inventors envision a protocol in which selective growth of fabrics of semiconducting or metallic nanotubes followed by etching would produce nanotube ribbons or traces useful in fabrication of electromechanical devices.

With regard to removal of undesired species, methods are known, for example, to process MWNTs and SWNT ropes to convert such into metallic or semidconducting

nanotubes as desired. (See Collins et al., Engineering Carbon Nanotubes and Nanotube Circuits Using Electrical Breakdown, Vol. 292 Science 706 (2001).)

With regard to application of purified nanotubes, using proper bulk nanotube preparations which contain primarily metallic or semiconducting nanotubes would allow application of a nanotube fabric to a substrate. The application could be performed via spin coating of a nanotube stock solution onto a substrate, dipping a substrate into a nanotube stock solution, spraying of nanotube stock solutions onto a surface or other methods. Application of single-walled, multiwalled or mixtures of such nanotubes can be envisioned with subsequent patterning and etching to generate fabrics or traces of sufficient length and width to make electronic devices.

By way of example, figure 1B.2 is similar to figure 1B.1 and the description thereof is not repeated. In material part, the method of figure 1B.2 removes the optional step of annealing nanotubes found in figure 1B.1 and substitutes it with a selective removal of nanotubes, e.g., removing semiconducting nanotubes or metallic. By doing so the composition of the nanofabric may be controlled.

Figures 3G-H is similar to figures 3B-C and the descriptions thereof are not repeated. In material part, the method of figure 3G adds a selective removal 345 of nanotubes, e.g., removing semiconducting nanotubes or metallic; analogously, the method of figure 3H adds a selective removal 380 of nanotubes. By doing so the composition of the nanofabric may be controlled.

Figure 4C is similar to figures 4B and the description thereof is not repeated. In material part, the method of figure 4C adds a selective removal 450 of nanotubes, e.g., removing semiconducting nanotubes or metallic. By doing so the composition of the nanofabric may be controlled.

Figure 4D is similar to figure 4B and the description thereof is not repeated. In material part, the method of figure 4D substitutes the CVD step 440 of figure 4B with a selective growth 440' of nanotubes, in which the growth process affects the relative concentration of one type of nanotube as compared to another. By doing so the composition of the nanofabric may be controlled.

Under some of the above embodiments, the application of nanotubes may be iterative. Thus for example a nanofabric may be created and subsequently processed to remove semiconducting nanotubes and then another application of nanotubes may be applied. Repeated application and removal will increase the relative amount of metallic or semiconducting nanotubes in the resulting nanofabric.

Figure 5I is similar to figure 5B and the description thereof is not repeated. In material part, the method of figure 5I removes the optional annealing step 540 of figure 5B and adds a selective removal 550 of nanotubes, e.g., removing semiconducting nanotubes or metallic. By doing so the composition of the nanofabric may be controlled. This process step 550 can be iterated to generate a more dense nanofabric.

Figure 5J is similar to figure 5B and the description thereof is not repeated. In material part, the method of figure 5I removes the optional annealing step 540 of figure 5B and substitutes the dispersal step 530 with a new dispersal step 530', in which the nanotubes that are disperse have a controlled composition, e.g., selected amounts of metallic nanotubes. By doing so the composition of the nanofabric may be controlled. This process step 530' can be iterated to generate a more dense nanofabric.

Other embodiments

Catalyst deposited on substrate surface or remaining in spun-on SWNTs may be removed by rinse/wash steps if a desired property of the ribbon included that it be free of metal/catalyst. This could be performed by successive treatments in an appropriate solvent or acid which would cause the removal of the exterior carbon shell that typically passivates the particles during nanotube growth. Other unreacted nanoparticles could be removed with just a mild solvent wash.

Some of the above methods of manufacturing such nanofabrics and patterning articles therefrom are conducive to certain environments, such as a circuit manufacturing environment. Other methods provide nanofabrics and articles therefrom that have desirable characteristics, such as an ability to adhere to hydrophobic surfaces (found in many electronic devices), even when the feature size is in the nanometer regime (<200 nm).

While the inventors typically desire a monolayer fabric of single-walled nanotubes, for certain applications it may be desirable to have multilayer fabrics to increase current density, redundancy or other mechanical or electrical characteristics. Additionally it may be desirable to use either a monolayer fabric or a multilayer fabric comprising MWNTs for certain applications or a mixture of single-walled and multi-walled nanotubes. The previous methods illustrate that control over catalyst type, catalyst distribution, surface derivitization, temperature, feedstock gas types, feedstock gas pressures and volumes, reaction time and other conditions allow growth of fabrics of single-walled, multi-walled or mixed single- and multi-walled nanotube fabrics that are at the least monolayers in nature but could be thicker as desired with measurable electrical characteristics.

In the case of formation of fabrics using pre-grown nanotubes, formulation of nanotube solutions in appropriate solvents with subsequent distribution over surfaces with or without derivitization allows exquisite control over porosity and density of the fabrics and would lead to facile generation of single-walled, multi-walled or mixed single- and multi-walled fabrics that are at the least monolayers in nature but could be thicker as desired with measurable electrical characteristics.

It will be further appreciated that the scope of the present invention is not limited to the above-described embodiments, but rather is defined by the appended claims, and that these claims will encompass modifications of and improvements to what has been described.

What is claimed is:

1. A conductive article having a plurality of nanotube segments in contact to define a plurality of conductive pathways along the article wherein the article has a shape of pre-defined pattern.
2. The article of claim 1 wherein the nanotube segments include single walled carbon nanotubes.
3. The article of claim 1 wherein the article is primarily a monolayer of nanotube segments.
4. The article of claim 1 wherein the article is about 2nm or less in thickness.
5. The article of claim 1 wherein the nanotube segments include multi-walled carbon nanotubes.
6. The article of claim 1 wherein the nanotube segments have different lengths.
7. The article of claim 1 wherein the nanotube segments include segments having a length shorter than the length of the article.
8. The article of claim 1 wherein the nanotube segments have a conformal characteristic to conform to non-planar surfaces.
9. The article of claim 1 wherein the article has a resistance of about 1-1000 k Ω /□.
10. The article of claim 1 wherein the article has a substantially uniform density of nanotube segments.
11. The article of claim 1 wherein the nanotube segments have a composition of metallic and semiconducting nanotubes and wherein the amount of metallic nanotubes relative to semiconducting nanotubes is a controlled amount.
12. An assembly including a substrate and a conductive trace disposed on the substrate, wherein the trace includes nanotube segments that contact other nanotube

segments to define a plurality of conductive pathways along the trace, wherein the trace has a shape of pre-defined pattern.

13. The assembly of claim 12 wherein the trace is about 2nm or less in thickness.
14. The assembly of claim 12 wherein the substrate has a non-planar surface and wherein the nanotube segments have a conformal characteristic to conform to the non-planar surface.
15. The assembly of claim 12 wherein the trace has a resistance of about 1-1000 k Ω /□.
16. The assembly of claim 12 wherein the nanotube segments include single walled carbon nanotubes.
17. The assembly of claim 12 wherein the trace has a substantially uniform density of nanotube segments.
18. The assembly of claim 12 wherein the nanotube segments include single walled carbon nanotubes.
19. The assembly of claim 12 wherein the trace is a multilayer of nanotube segments.
20. The assembly of claim 12 wherein the nanotube segments include multi-walled carbon nanotubes.
21. The assembly of claim 12 wherein the nanotube segments have different lengths.
22. The assembly of claim 12 wherein the nanotube segments include segments having a length shorter than the length of the article.
23. The assembly of claim 12 wherein the trace is primarily a monolayer of nanotube segments.

24. The assembly of claim 12 wherein the nanotube segments have a composition of metallic and semiconducting nanotubes and wherein the amount of metallic nanotubes relative to semiconducting nanotubes is a controlled amount.

25. A conductive trace comprising an electrical network of nanotubes in contact with other nanotubes to define a plurality of conductive pathways along the trace wherein the trace has a shape of pre-defined pattern.

26. The conductive trace of claim 25 wherein the nanotube segments have a conformal characteristic to conform to the non-planar surfaces.

27. The conductive trace of claim 25 wherein the trace has a resistance of about 1-1000 k Ω /□.

28. The conductive trace of claim 25 wherein the nanotube segments include single walled carbon nanotubes.

29. The conductive trace of claim 25 wherein the trace has a substantially uniform density of nanotube segments.

30. The conductive trace of claim 25 wherein the nanotubes include multi-walled carbon nanotubes.

31. The conductive trace of claim 25 wherein the nanotubes have different lengths.

32. The conductive trace of claim 25 wherein the nanotubes include segments having a length shorter than the length of the trace.

33. The conductive trace of claim 25 wherein the trace is primarily a monolayer of nanotube segments.

34. The conductive trace of claim 25 wherein the trace is about 2nm or less in thickness.

35. The conductive trace of claim 25 wherein the nanotube segments have a composition of metallic and semiconducting nanotubes and wherein the amount of metallic nanotubes relative to semiconducting nanotubes is a controlled amount.

36. A non-woven fabric of carbon nanotubes in contact wherein the fabric has substantially uniform density.

37. The non-woven fabric of claim 36 having a controlled composition of metallic nanotubes relative to semiconducting nanotubes.

38. The non-woven fabric of claim 36 wherein the fabric is primarily single-walled carbon nanotubes.

39. The non-woven fabric of claim 36 wherein the fabric is primarily multi-walled carbon nanotubes.

40. The non-woven fabric of claim 36 wherein the fabric is primarily a monolayer of nanotube segments.

41. The non-woven fabric of claim 36 wherein the fabric is primarily a multilayer of nanotube segments.

42. The non-woven fabric of claim 36 wherein the fabric is about 2nm or less in thickness.

43. The non-woven fabric of claim 36 wherein the fabric has a conformal characteristic to conform to non-planar surfaces.

44. The non-woven fabric of claim 36 wherein the fabric has a resistance of about 1-1000 k Ω /□.

Figure 1A



Figure 1B.1

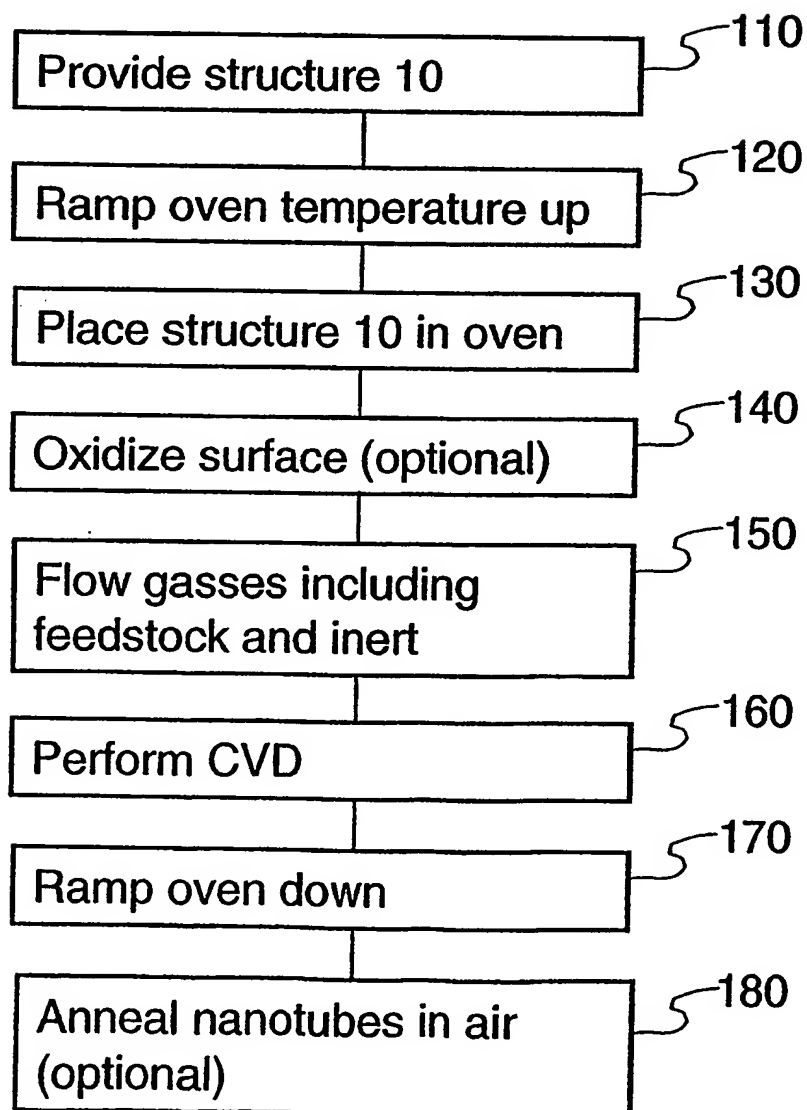


Figure 1B.2

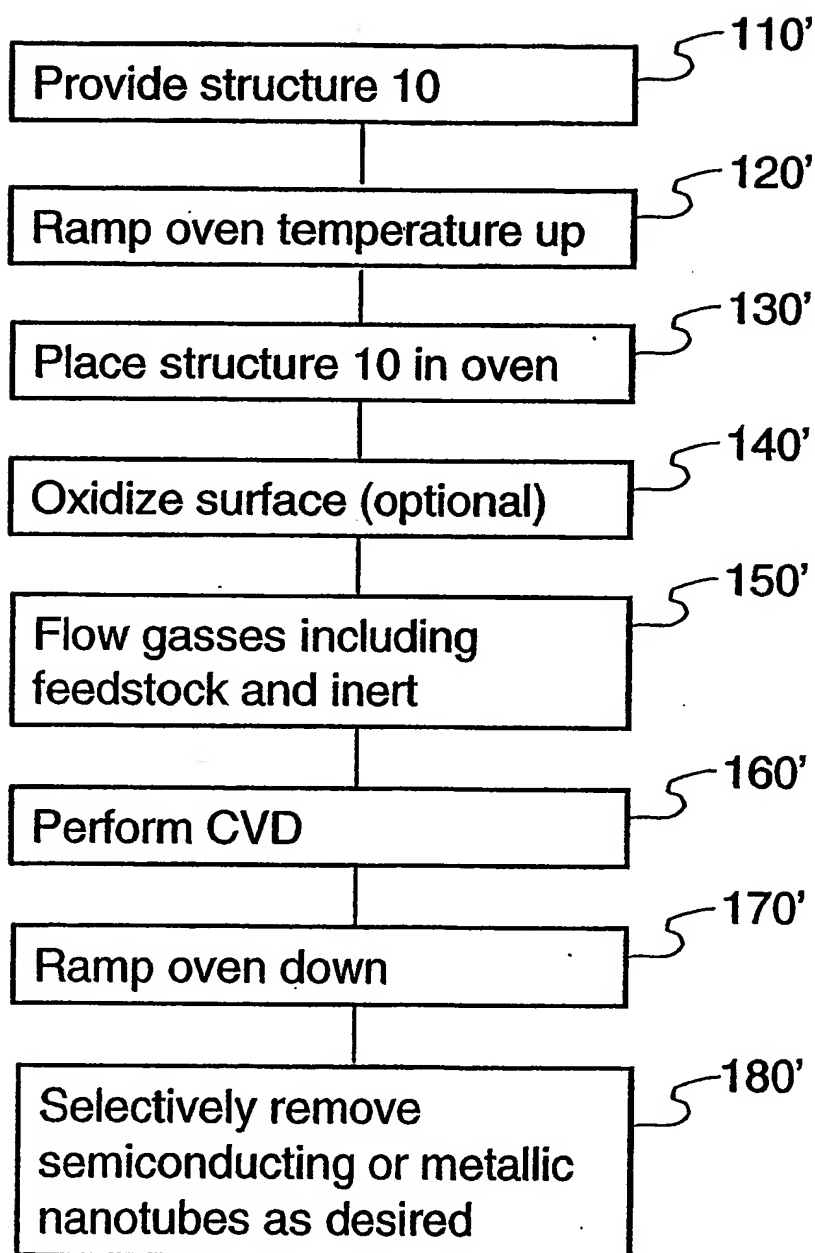
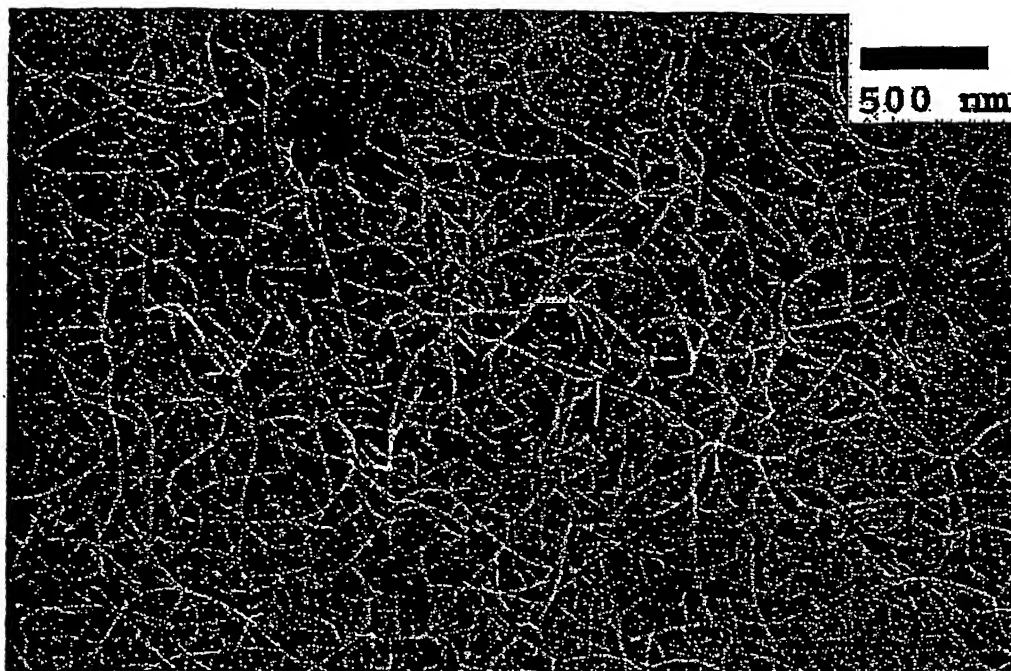


Figure 1C



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Figure 1D

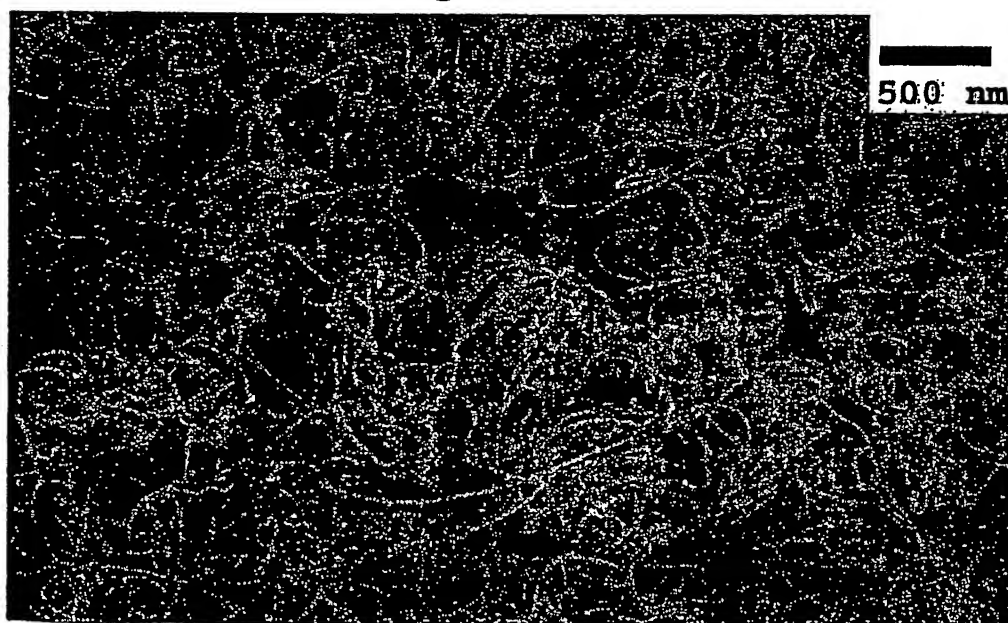
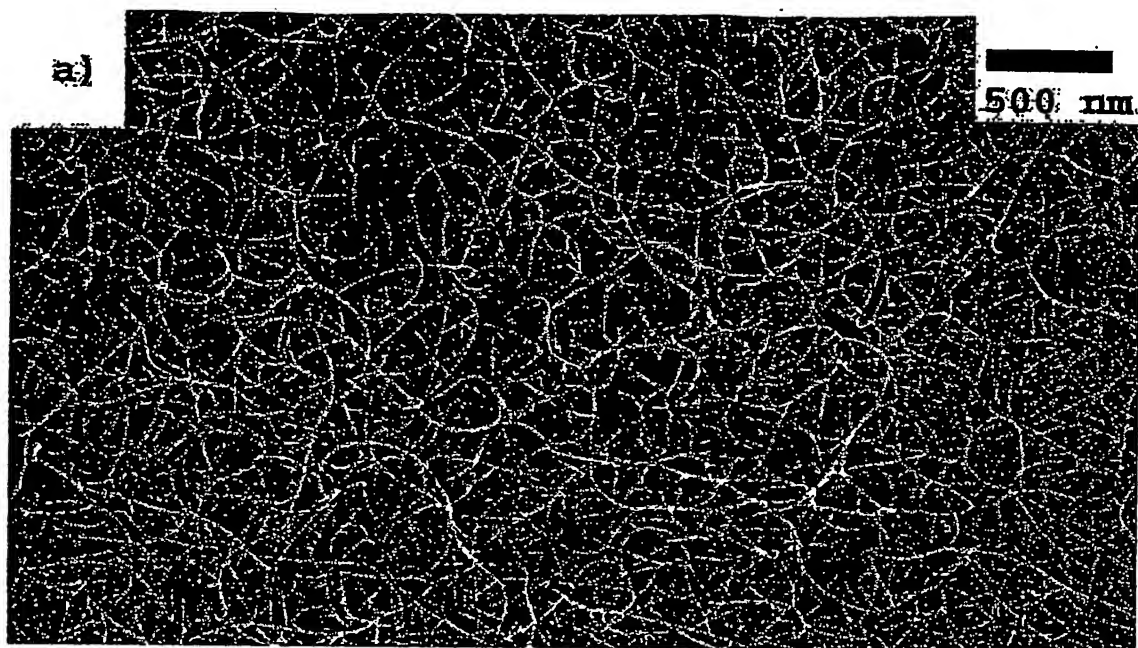


Figure 1E



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Figure 1F

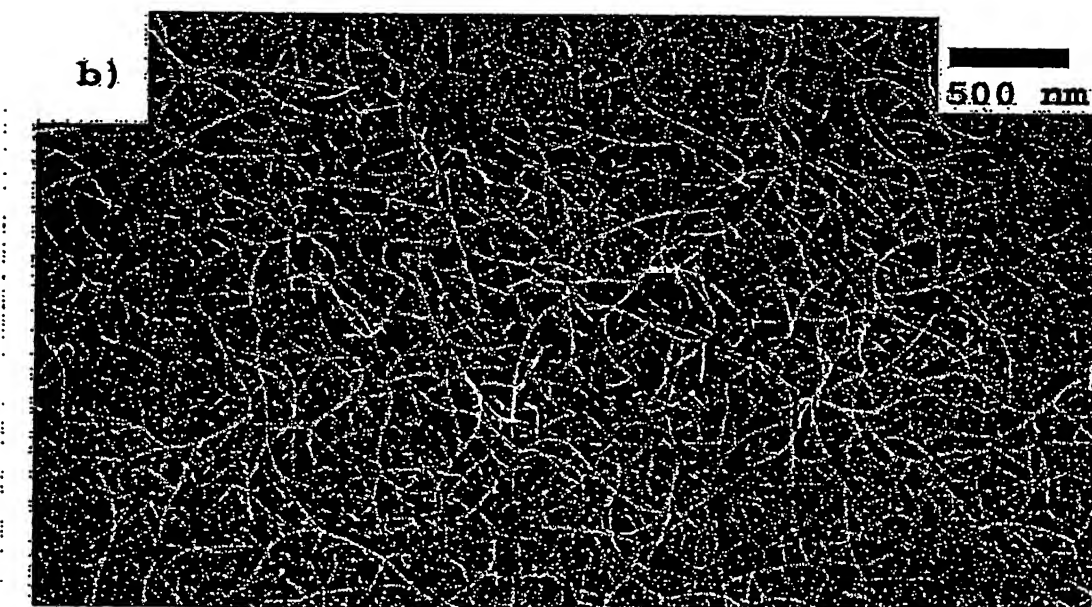
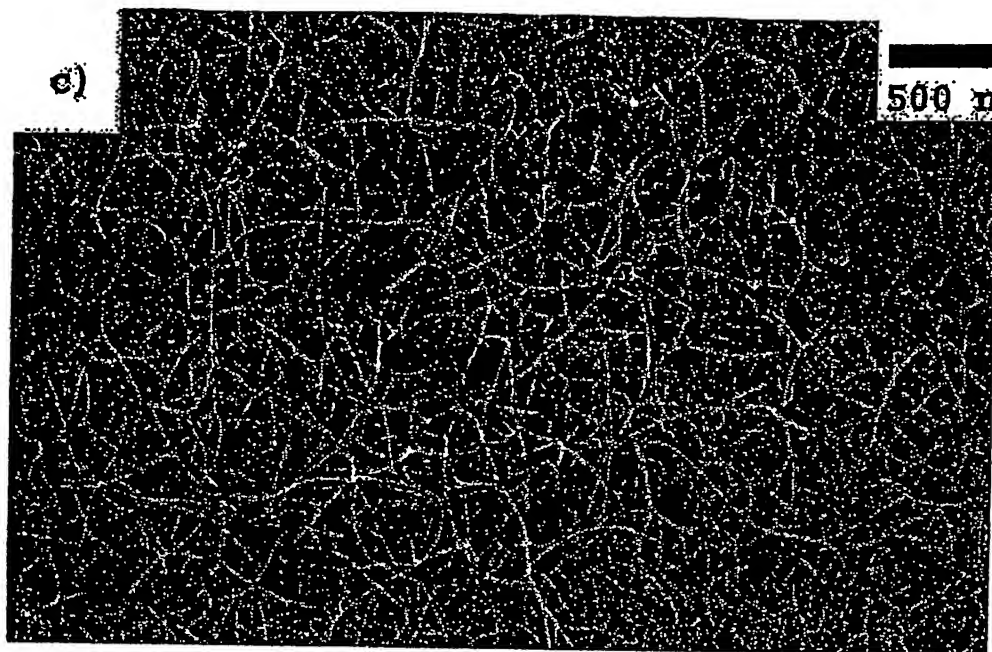


Figure 1G



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Figure 1H

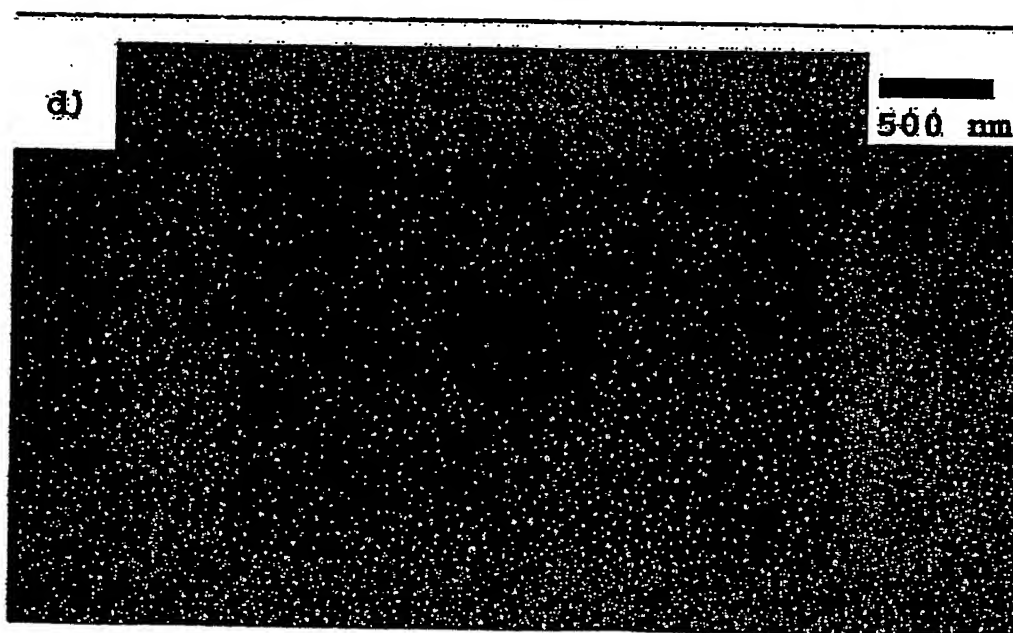


Figure 1I

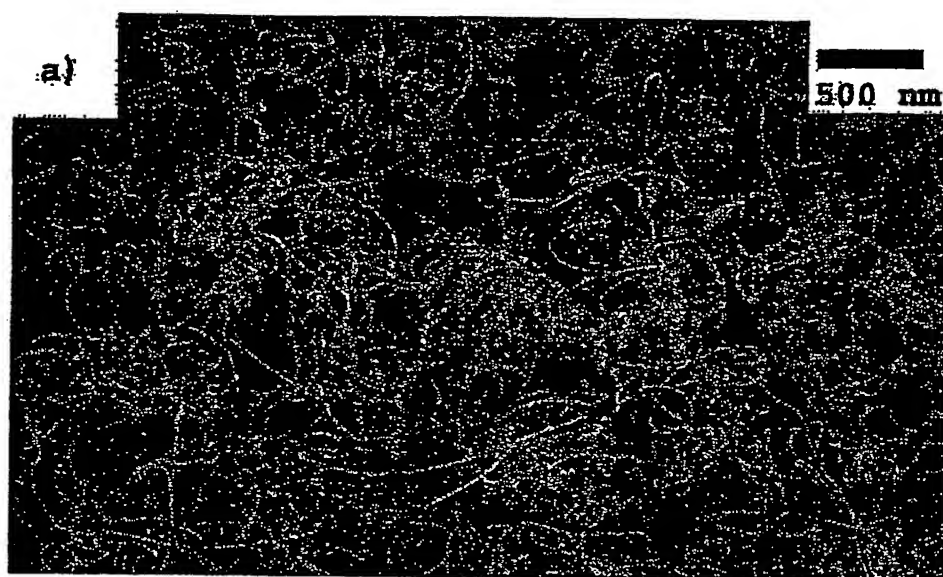
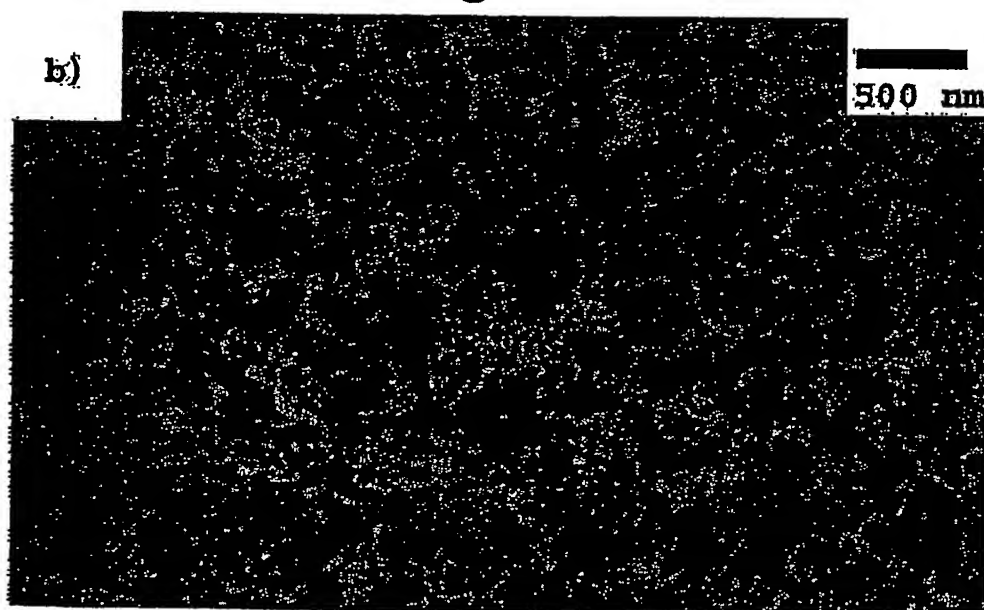


Figure 1J



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Figure 1K

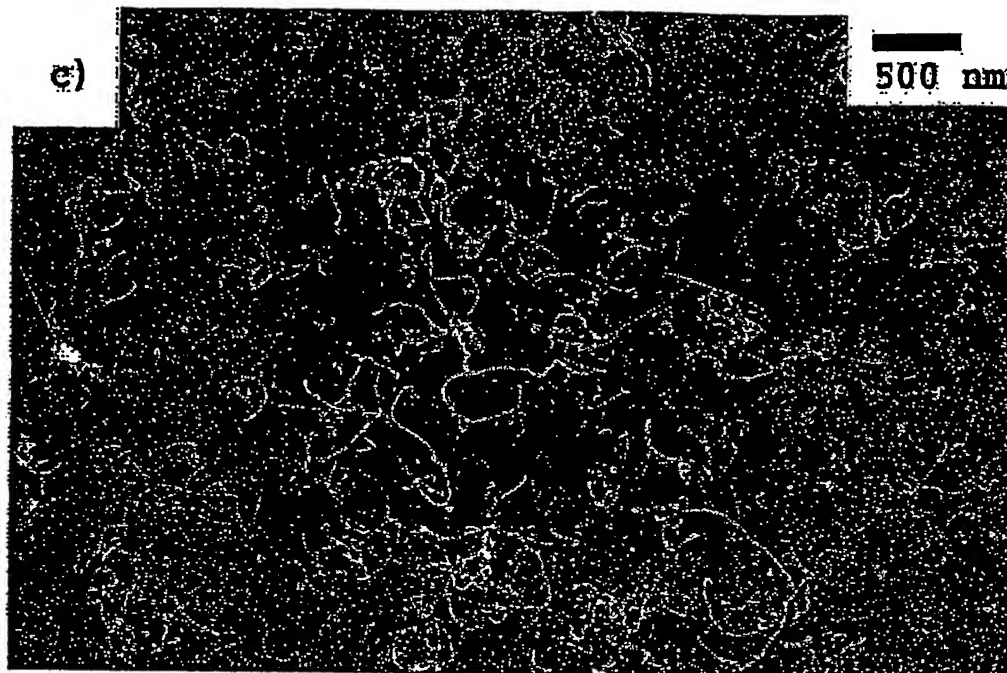


Figure 1L

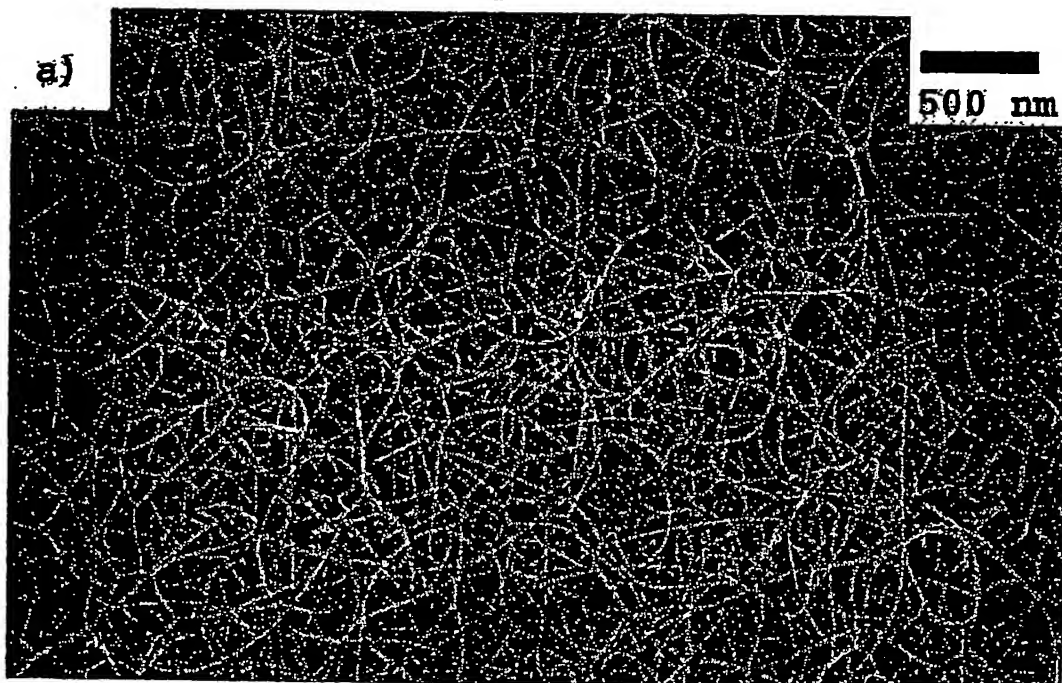


Figure 1M

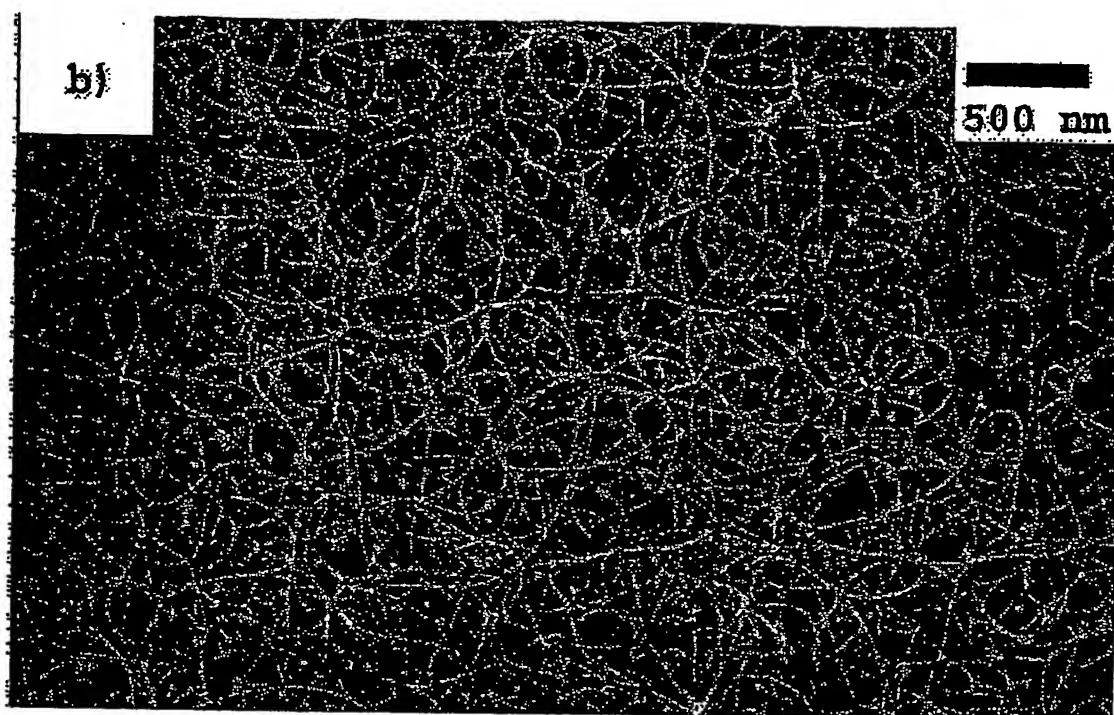
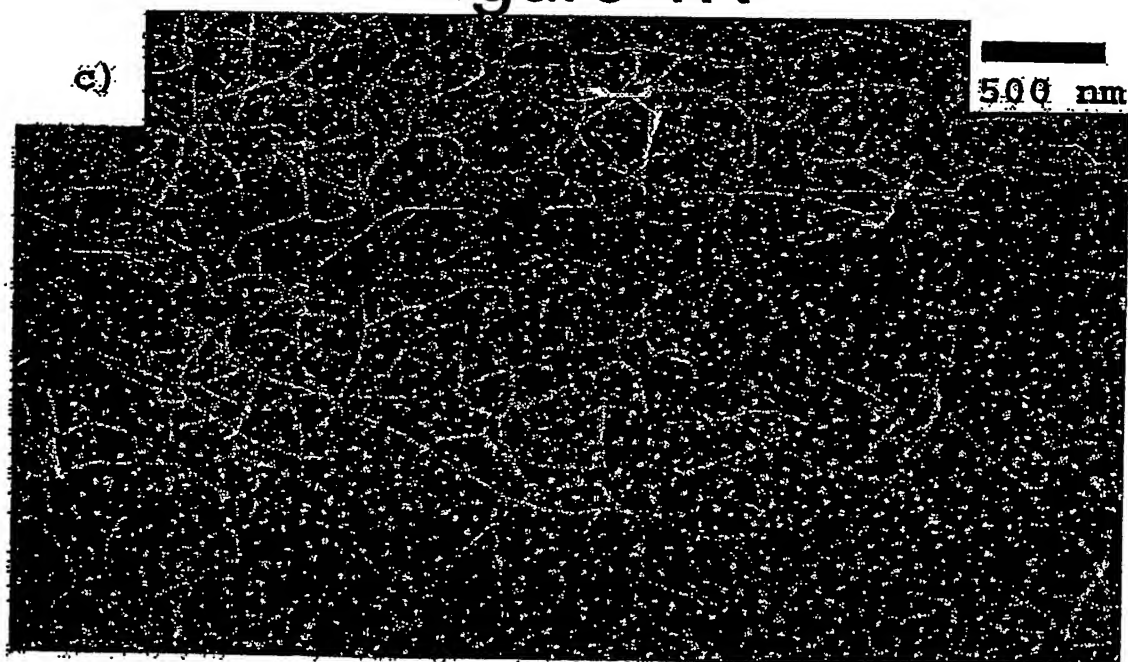


Figure 1N



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Figure 10

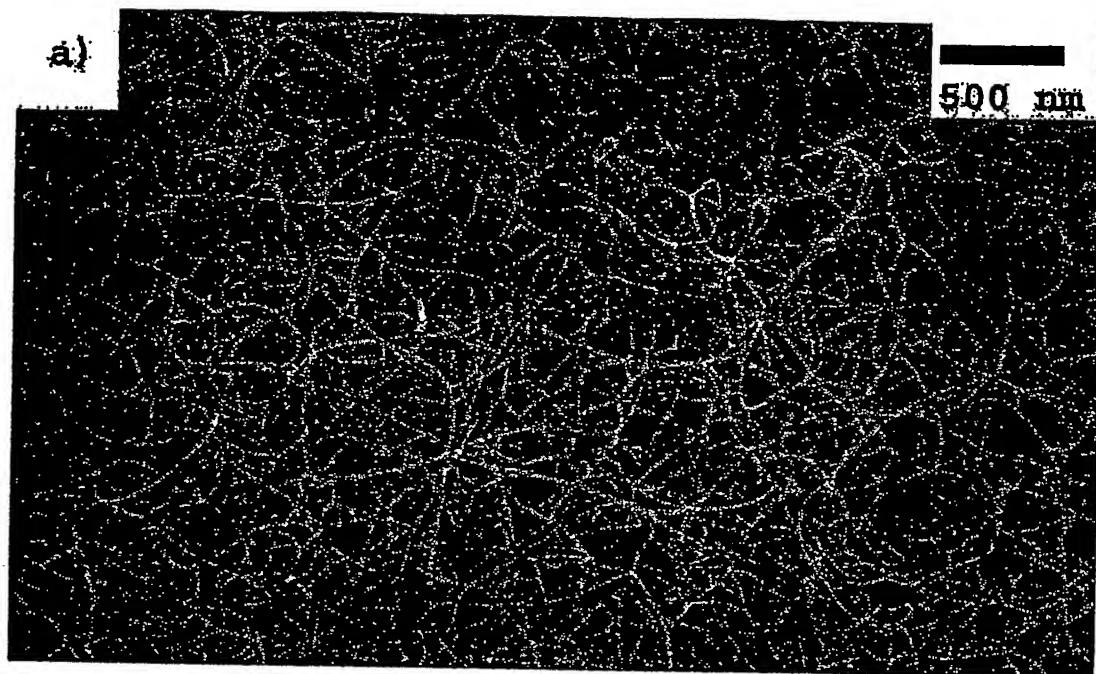
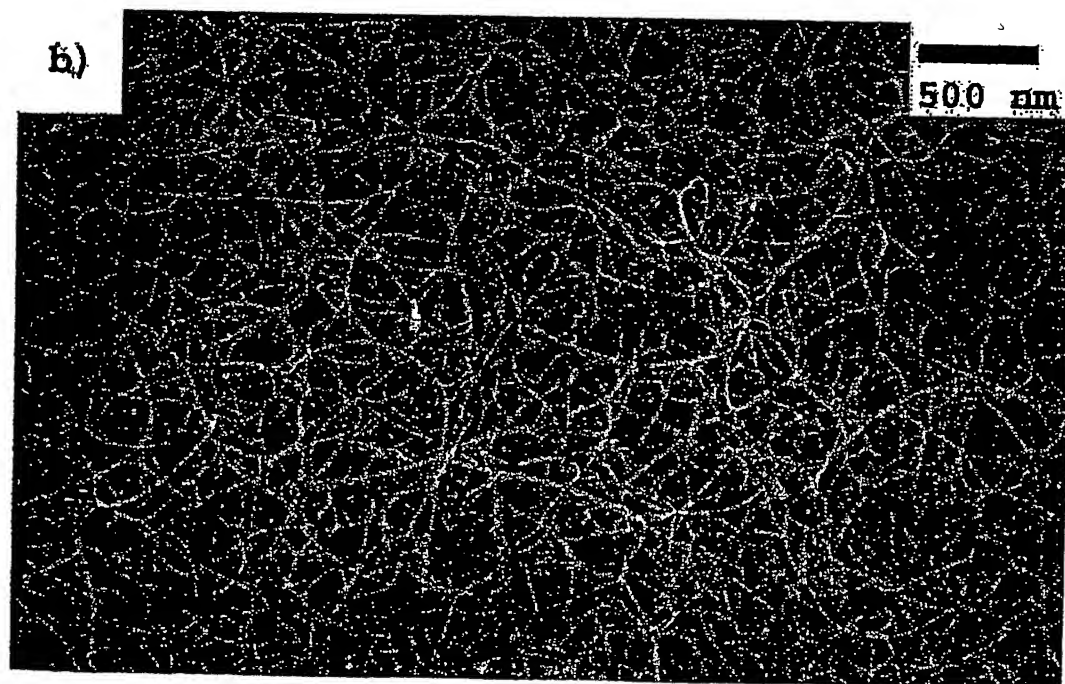


Figure 1P



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Figure 1Q

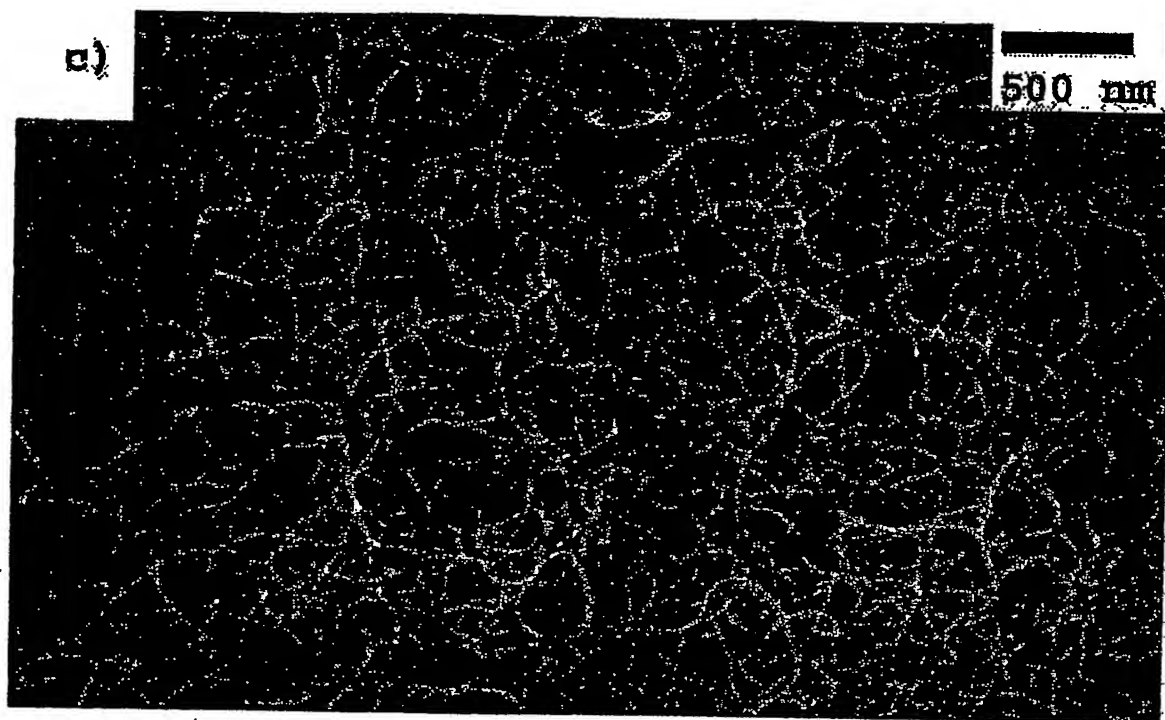
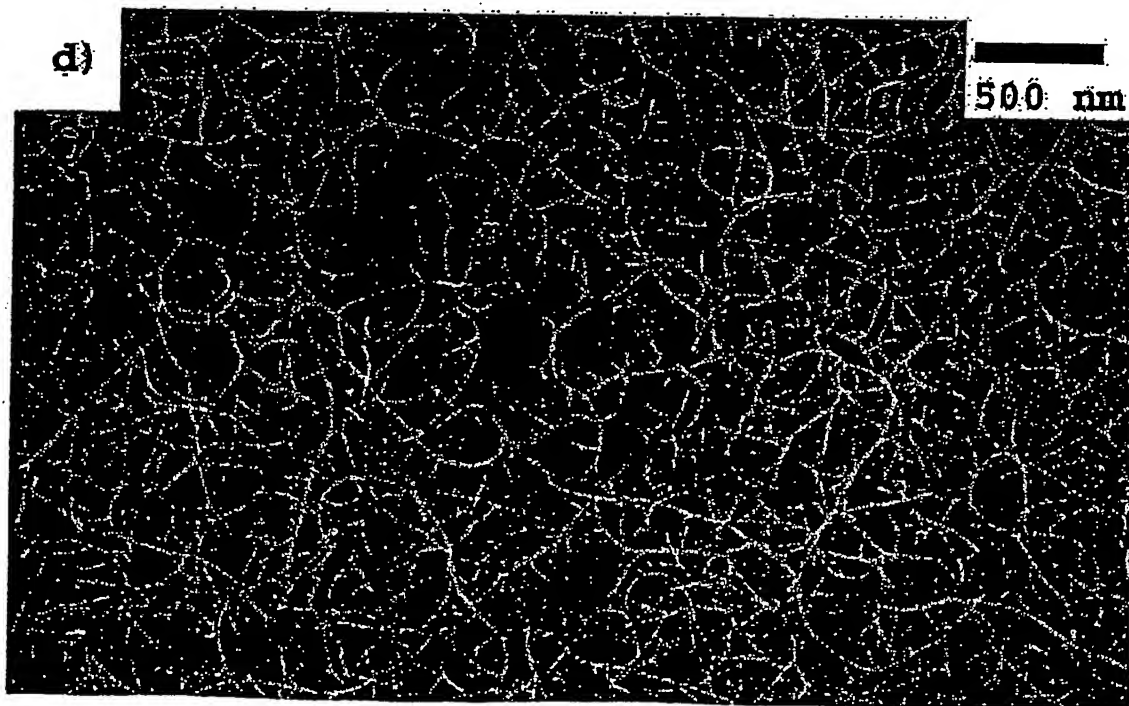


Figure 1R



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Figure 1S

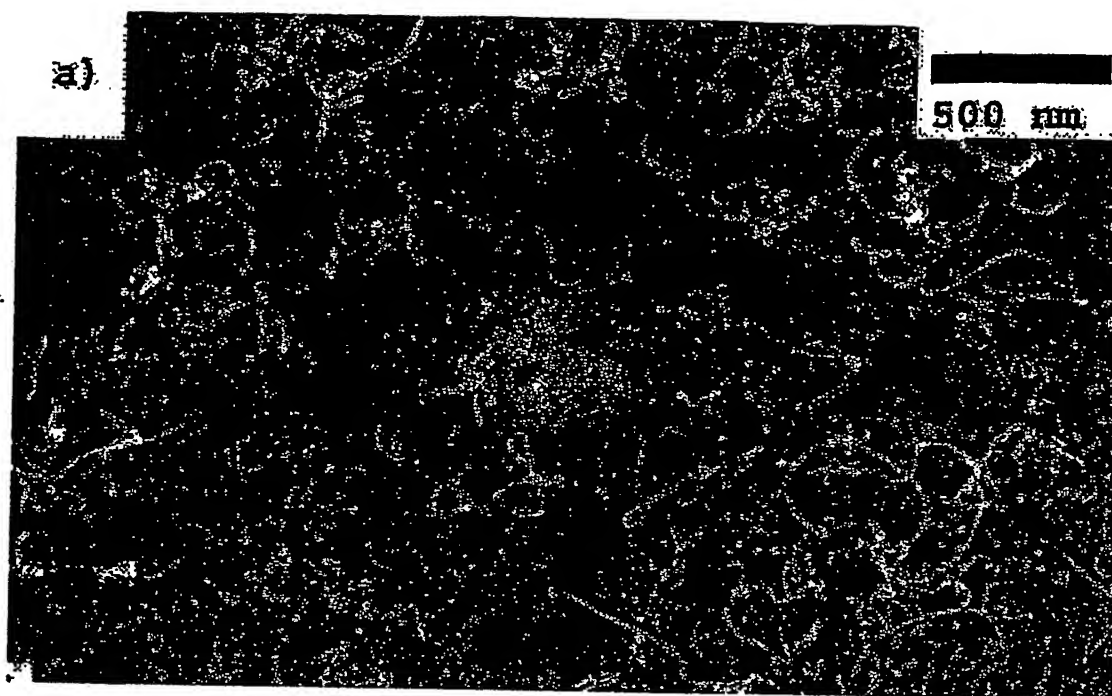
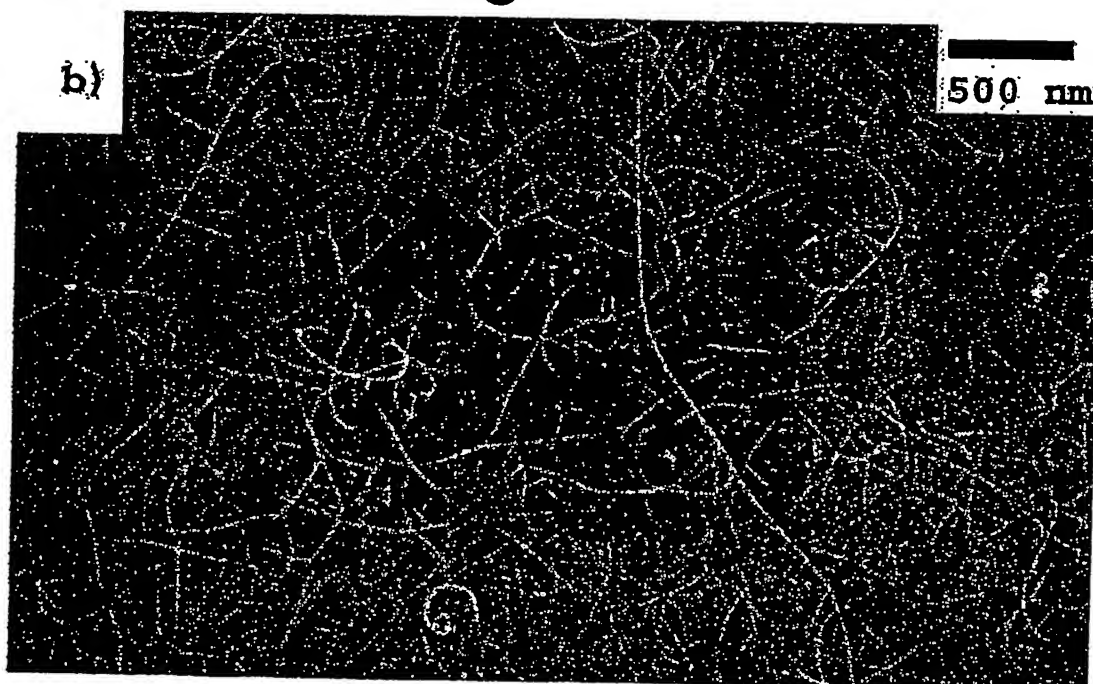


Figure 1T



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Figure 1U

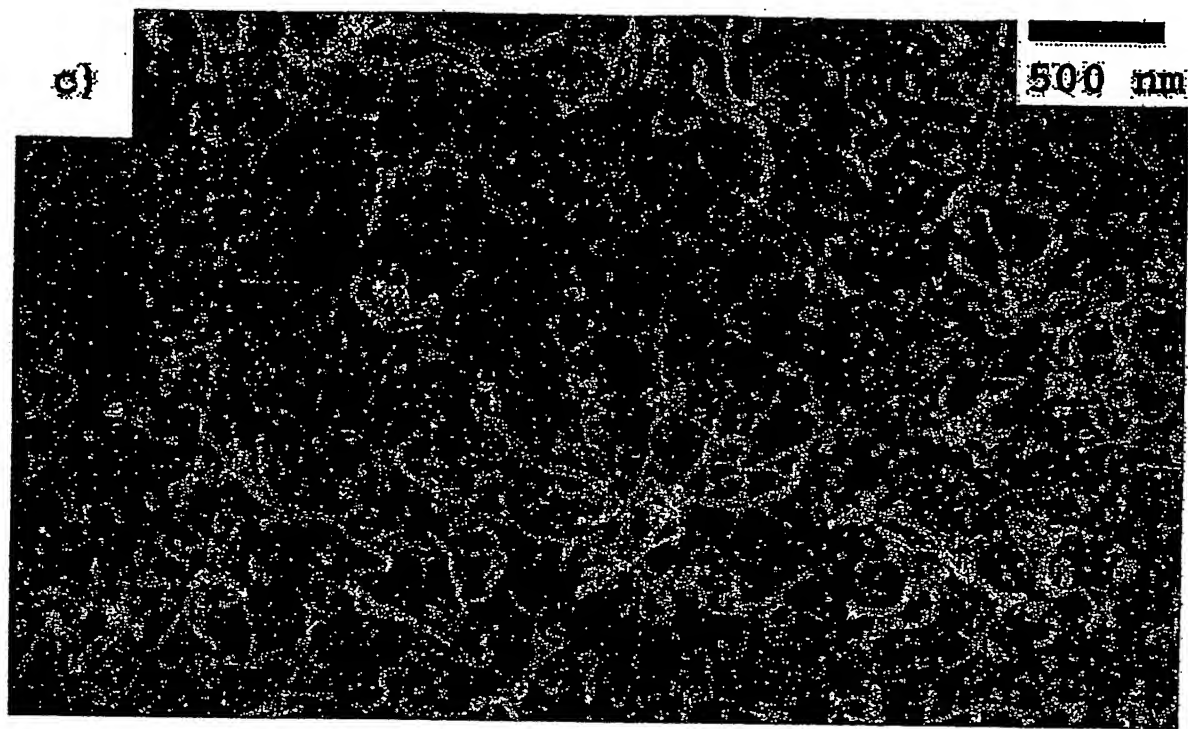
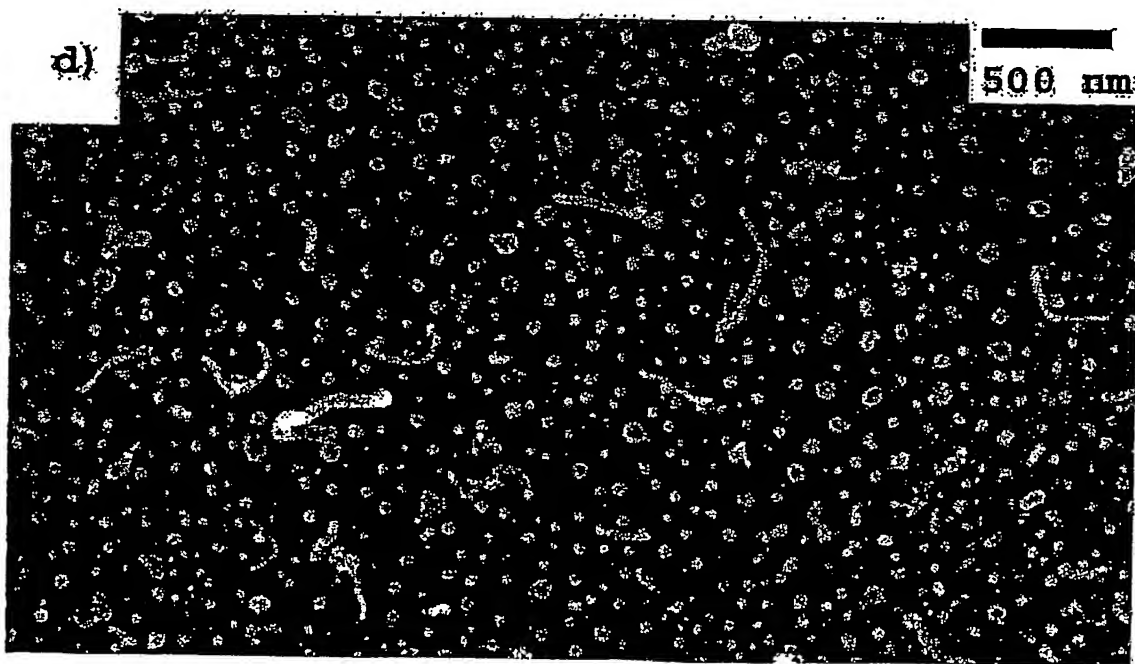
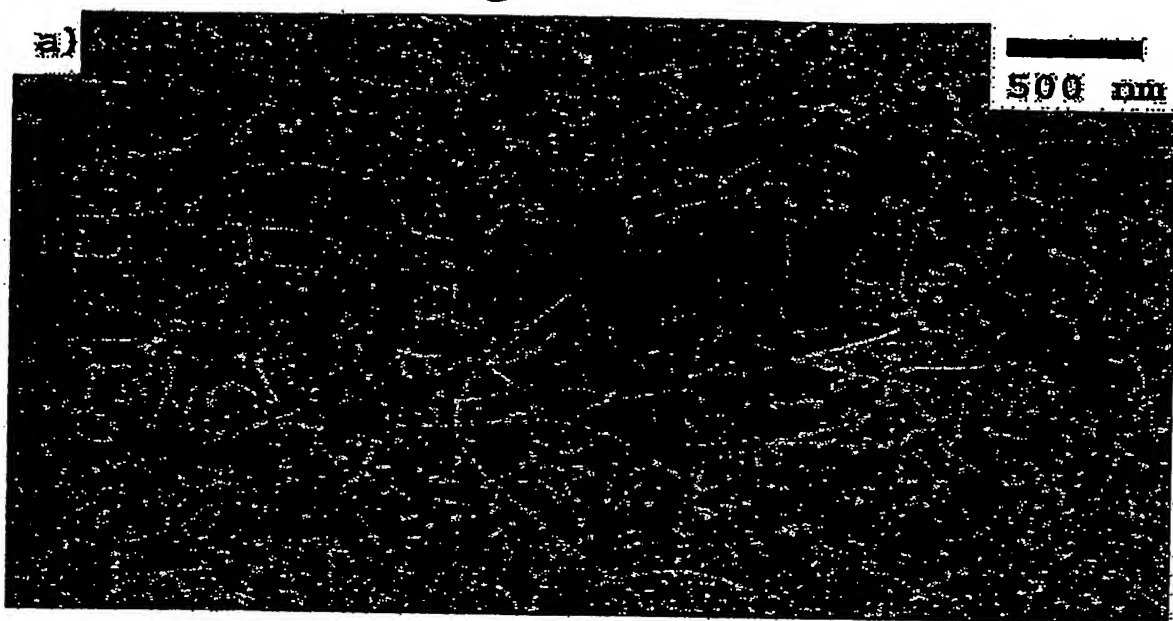


Figure 1V



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Figure 1W



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Figure 1X

